



Click [here](#) for the 3D model.

Dimensions

Chip Size	0805
L	2mm +/-0.2mm
W	1.25mm +/-0.2mm
T	0.9mm +/-0.10mm
S	0.75mm MIN
B	0.5mm +/-0.25mm

Packaging Specifications

Packaging	T&R, 330mm, Paper Tape
Packaging Quantity	15000

General Information

Series	ESD SMD Comm COG
Style	SMD Chip
Description	SMD, MLCC, Temperature Stable, Electro Static Discharge, Class I
Features	Temperature Stable, Low ESR, Class I
RoHS	Yes
Termination	Tin
Marking	No
AEC-Q200	No
Component Weight	13 mg
Shelf Life	78 Weeks
MSL	1

Specifications

Capacitance	1000 pF
Measurement Condition	1 MHz 1.0Vrms
Capacitance Tolerance	10%
Voltage DC	200 VDC
ESD Level per AEC-Q200	8,000 V ESD Level
Dielectric Withstanding Voltage	500 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1MegaHz 1.0Vrms
Dissipation Factor	0.1% 1 MHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	100 GOhms

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.