

## N and P-Channel Enhancement Mode Power MOSFET

### Description

The RM2020ES9 uses advanced trench technology to provide excellent  $R_{DS(ON)}$  and low gate charge. The complementary MOSFETs may be used to form a level shifted high side switch, and for a host of other applications.

### General Features

#### ● N-Channel

$$V_{DS} = 20V, I_D = 0.75A$$

$$R_{DS(ON)} < 380 \text{ m}\Omega @ V_{GS}=4.5V$$

$$R_{DS(ON)} < 450 \text{ m}\Omega @ V_{GS}=2.5V$$

$$R_{DS(ON)} < 800 \text{ m}\Omega @ V_{GS}=1.8V$$

#### ● P-Channel

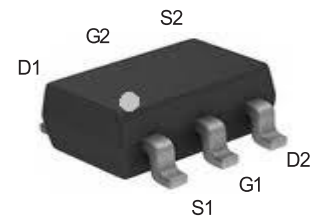
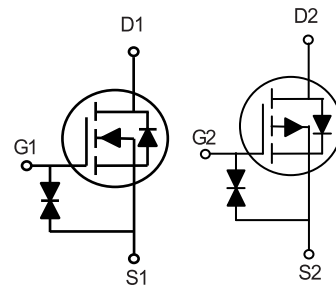
$$V_{DS} = -20V, I_D = -0.8A$$

$$R_{DS(ON)} < 800 \text{ m}\Omega @ V_{GS}=-4.5V$$

$$R_{DS(ON)} < 1200 \text{ m}\Omega @ V_{GS}=-2.5V$$

- High power and current handling capability
- Lead free product is acquired
- Surface mount package
- Halogen-free

### Equivalent Circuit



### SOT-363

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
2020	RM2020ES9	SOT-363-6L	Ø180mm	8mm	3000units

### Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	$V_{DS}$	20	-20 (typ.-25)	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	$\pm 12$	V
Continuous Drain Current	$I_D$	0.75	-0.8	A
Pulsed Drain Current <sup>(Note 1)</sup>	$I_{DM}$	5	-4	A
Maximum Power Dissipation	$P_D$	0.15	0.8	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	-55 To 150	$^\circ\text{C}$

### Thermal Characteristic

Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	N-Ch	833	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	P-Ch	156	$^\circ\text{C/W}$

## Electrical Characteristics ( $T_A = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted )

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_{DS} = -250\text{ }\mu\text{A}$	-20	-	-	V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{DS} = -250\text{ }\mu\text{A}$	-0.3	-0.65	-1.0	V
$I_{DSS}$	Drain Leakage Current	$V_{DS} = -20\text{ V}, V_{GS} = 0\text{ V}$	-	-	-1	$\mu\text{A}$
		$T_J = 85\text{ }^\circ\text{C}$	-	-	-30	$\mu\text{A}$
$I_{GSS}$	Gate Leakage Current	$V_{GS} = \pm 8\text{ V}, V_{DS} = 0\text{ V}$	-	-	$\pm 10$	$\mu\text{A}$
$R_{DS(ON)}^a$	On-State Resistance	$V_{GS} = -4.5\text{ V}, I_{DS} = -0.5\text{ A}$	-	0.85	1.2	$\Omega$
		$V_{GS} = -2.5\text{ V}, I_{DS} = -0.2\text{ A}$	-	1.05	1.5	
		$V_{GS} = -1.5\text{ V}, I_{DS} = -0.04\text{ A}$	-	1.5	-	
		$V_{GS} = -1.2\text{ V}, I_{DS} = -0.01\text{ A}$	-	2	-	
<b>Diode Characteristics</b>						
$V_{SD}^a$	Diode Forward Voltage	$I_{SD} = -0.5\text{ A}, V_{GS} = 0\text{ V}$	-	-	1.3	V
$t_{rr}$	Reverse Recovery Time	$I_{SD} = -0.5\text{ A}, dI_{SD}/dt = 100\text{ A}/\mu\text{s}$	-	70	-	ns
$Q_{rr}$	Reverse Recovery Charge		-	68	-	nC
<b>Dynamic Characteristics<sup>b</sup></b>						
$C_{iss}$	Input Capacitance	$V_{GS} = 0\text{ V}, V_{DS} = -10\text{ V}$ Frequency = 1 MHz	-	87	-	pF
$C_{oss}$	Output Capacitance		-	15	-	
$C_{rss}$	Reverse Transfer Capacitance		-	8.2	-	
$t_d(on)$	Turn-on Delay Time	$V_{DS} = -30\text{ V}, V_{GEN} = -10\text{ V},$ $R_G = 25\text{ }\Omega, R_L = 60\text{ }\Omega,$ $I_{DS} = -0.67\text{ A}$	-	5.6	-	ns
$t_r$	Turn-on Rise Time		-	5.3	-	
$t_d(off)$	Turn-off Delay Time		-	30	-	
$t_f$	Turn-off Fall Time		-	21	-	
$Q_g$	Total Gate Charge	$V_{GS} = -4.5\text{ V}, V_{DS} = -10\text{ V},$ $I_{DS} = -0.67\text{ A}$	-	1.8	-	nC
$Q_{gs}$	Gate-Source Charge		-	0.82	-	
$Q_{gd}$	Gate-Drain Charge		-	0.59	-	

Notes :

a : Pulse test ; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$

b : Guaranteed by design, not subject to production testing

**MOSFET ELECTRICAL CHARACTERISTICS  $T_a=25^\circ\text{C}$  unless otherwise specified**

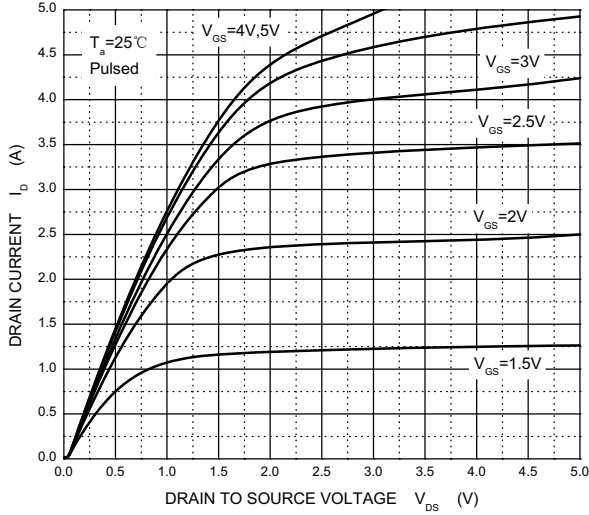
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>STATIC PARAMETERS</b>						
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	20			V
Zero gate voltage drain current	$I_{DSS}$	$V_{DS} = 20V, V_{GS} = 0V$			1	$\mu A$
Gate-body leakage current	$I_{GSS}$	$V_{GS} = \pm 10V, V_{DS} = 0V$			$\pm 20$	$\mu A$
Gate threshold voltage (note 1)	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	0.35	0.54	1.1	V
Drain-source on-resistance (note 1)	$R_{DS(on)}$	$V_{GS} = 4.5V, I_D = 0.65A$		270	380	$m\Omega$
		$V_{GS} = 2.5V, I_D = 0.55A$		320	450	$m\Omega$
		$V_{GS} = 1.8V, I_D = 0.45A$		390	800	$m\Omega$
Forward tranconductance (note 1)	$g_{FS}$	$V_{DS} = 10V, I_D = 0.8A$		1.6		S
Diode forward voltage(note 1)	$V_{SD}$	$I_S = 0.15A, V_{GS} = 0V$			1.2	V
<b>DYNAMIC PARAMETERS (note 2)</b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 16V, V_{GS} = 0V, f = 1MHz$		79	120	pF
Output Capacitance	$C_{oss}$			13	20	pF
Reverse Transfer Capacitance	$C_{rss}$			9	15	pF
<b>SWITCHING PARAMETERS(note 2)</b>						
Turn-on delay time	$t_{d(on)}$	$V_{GS} = 4.5V, V_{DS} = 10V, I_D = 0.5A, R_{GEN} = 10\Omega$		6.7		ns
Turn-on rise time	$t_r$			4.8		ns
Turn-off delay time	$t_{d(off)}$			17.3		ns
Turn-off fall time	$t_f$			7.4		ns
Total Gate Charge	$Q_g$	$V_{DS} = 10V, V_{GS} = 4.5V, I_D = 0.25A$		1.2		nC
Gate-Source Charge	$Q_{gs}$			0.28		nC
Gate-Drain Charge	$Q_{gd}$			0.2		nC

**Notes :**

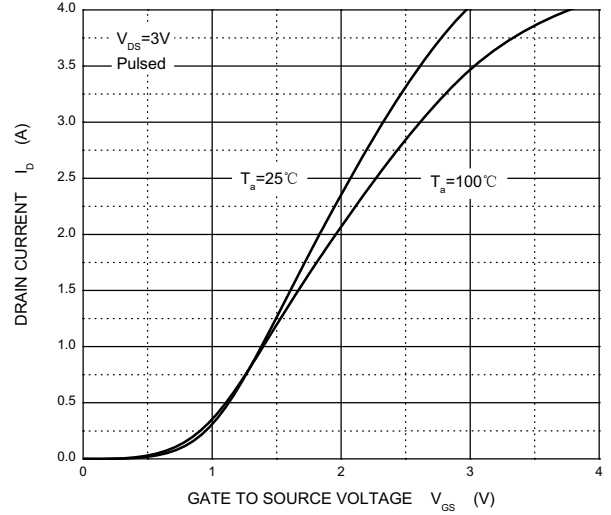
1. Pulse Test : Pulse width  $\leq 300\mu s$ , duty cycle  $\leq 0.5\%$ .
2. Guaranteed by design, not subject to production testing.

# N-Typical Characteristics

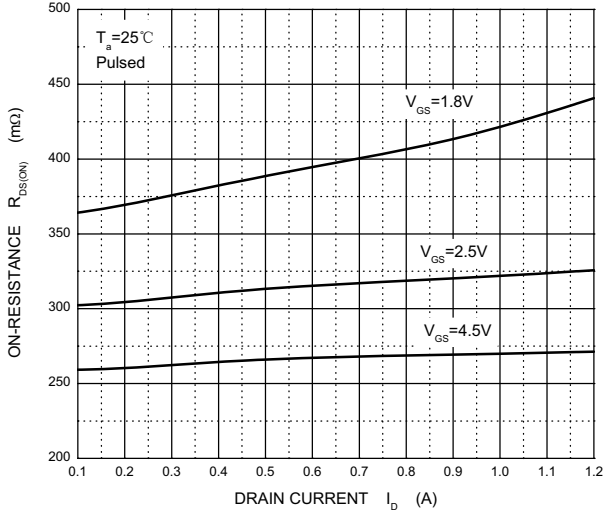
## Output Characteristics



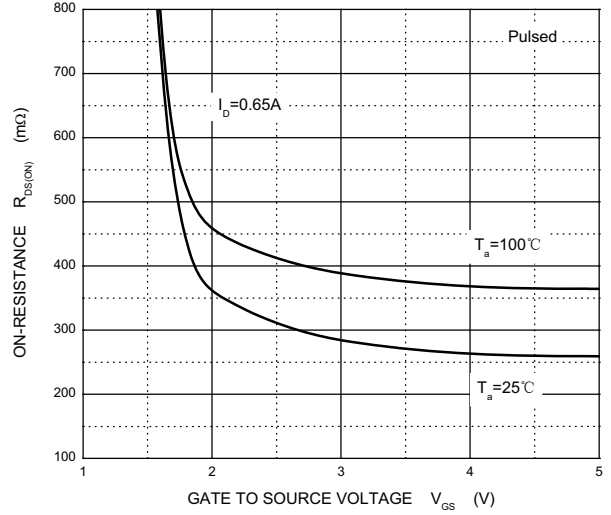
## Transfer Characteristics



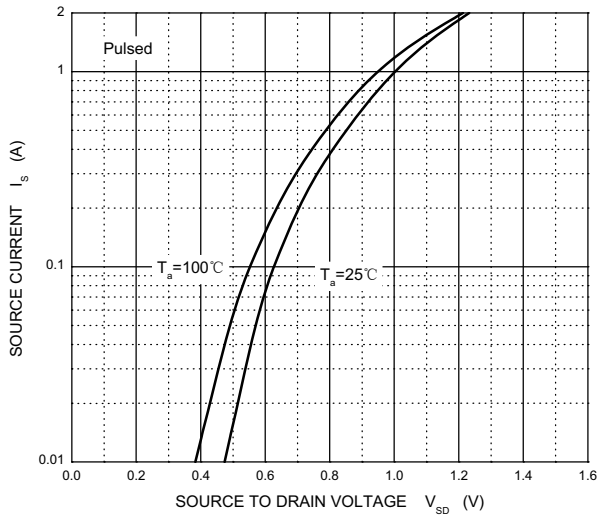
## $R_{DS(ON)}$ — $I_D$



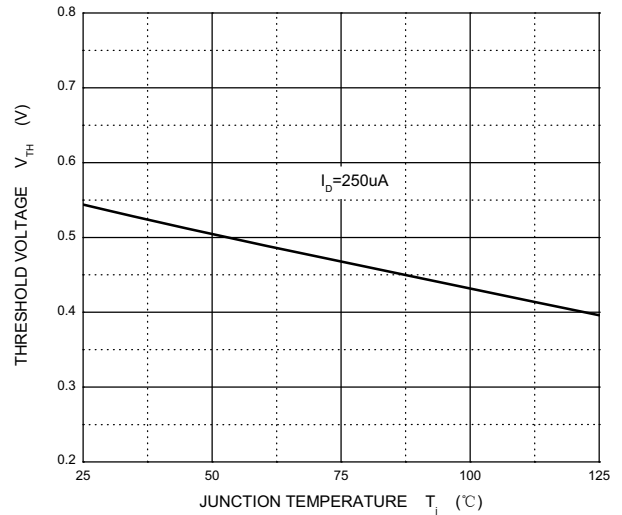
## $R_{DS(ON)}$ — $V_{GS}$



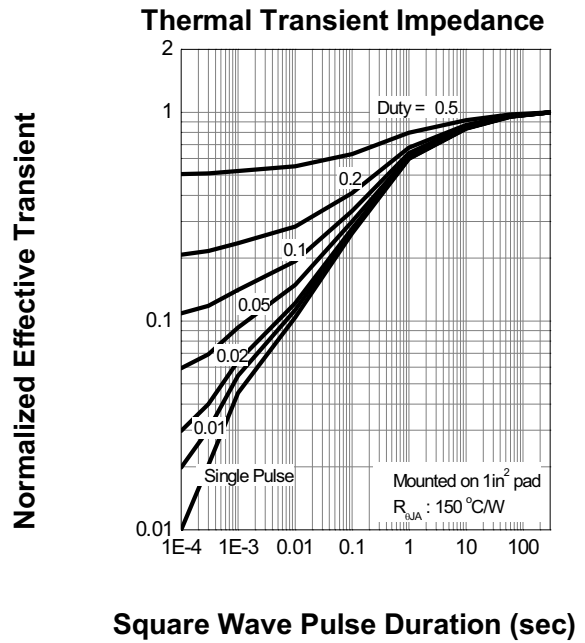
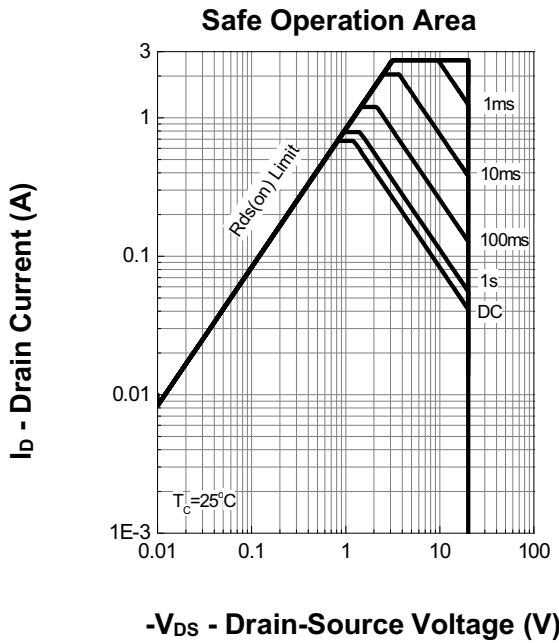
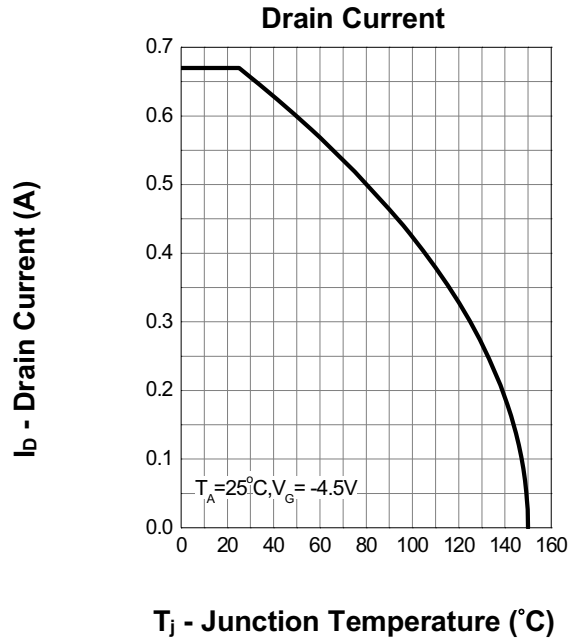
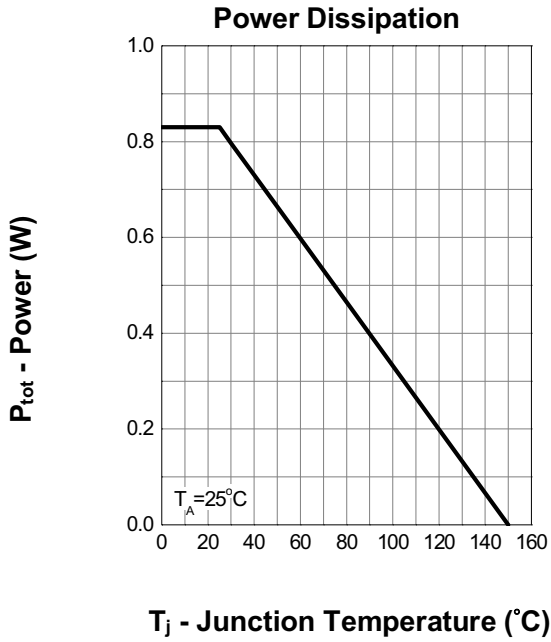
## $I_S$ — $V_{SD}$



## Threshold Voltage

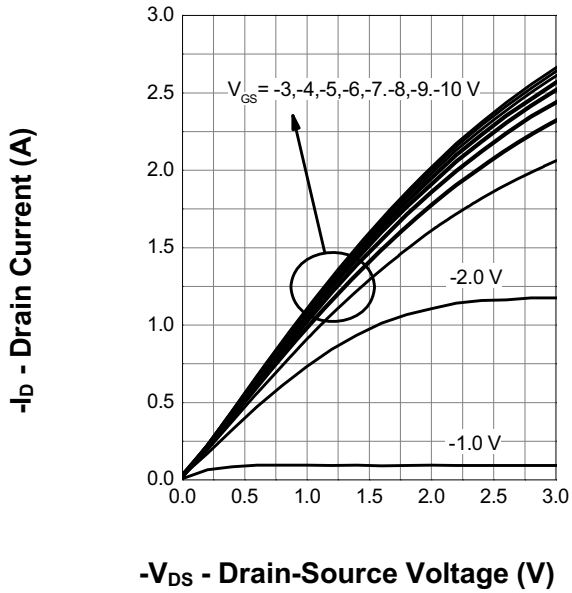


# P-Typical Characteristics

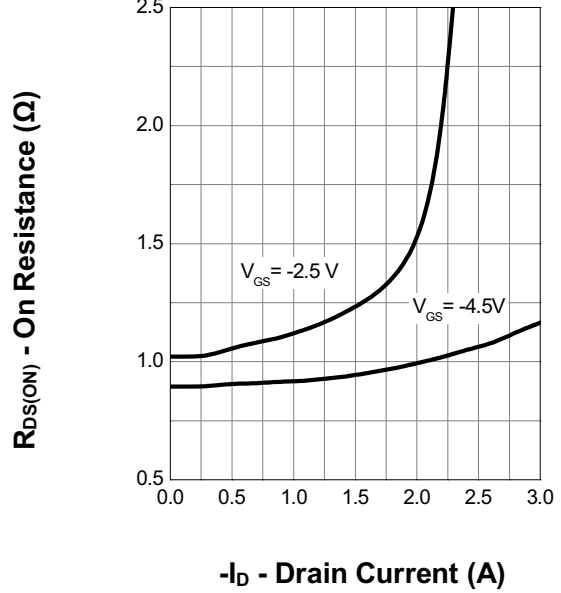


# P-Typical Characteristics

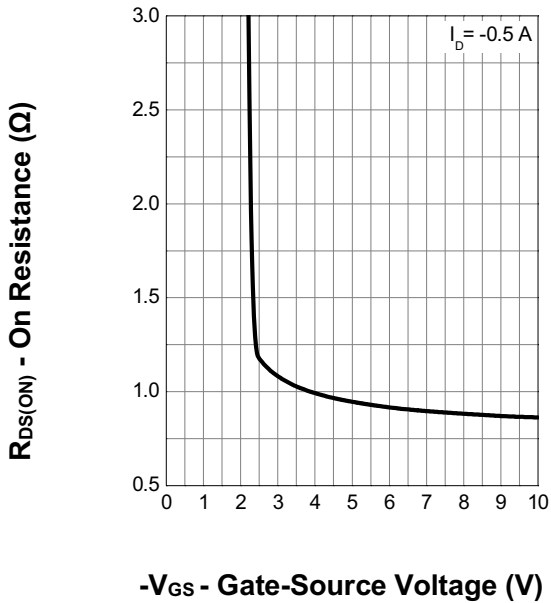
**Output Characteristics**



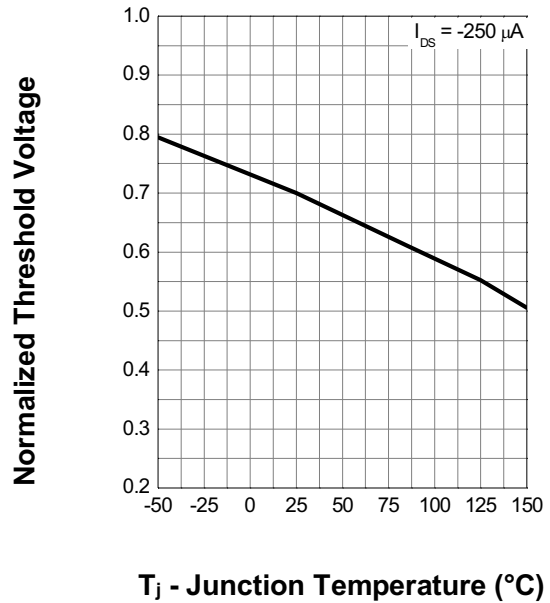
**Drain-Source On Resistance**



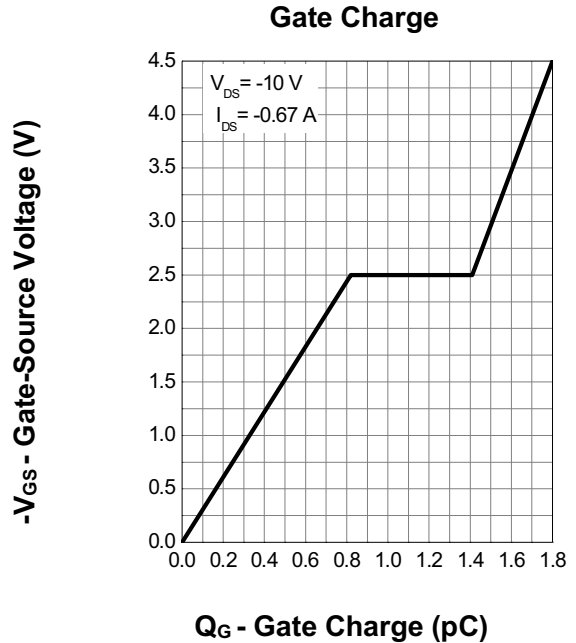
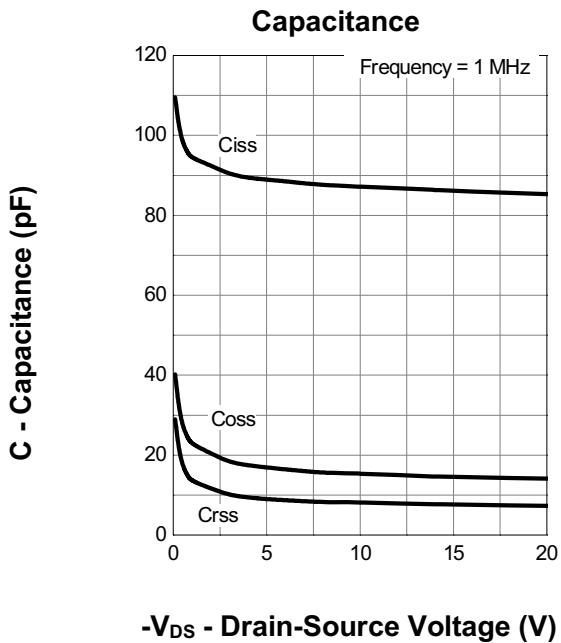
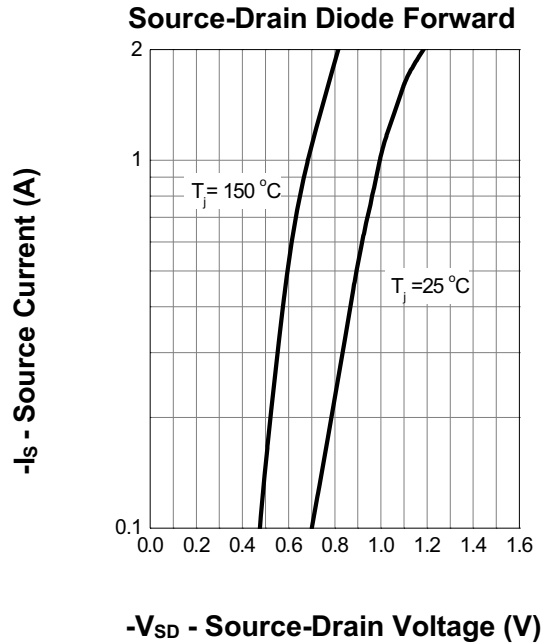
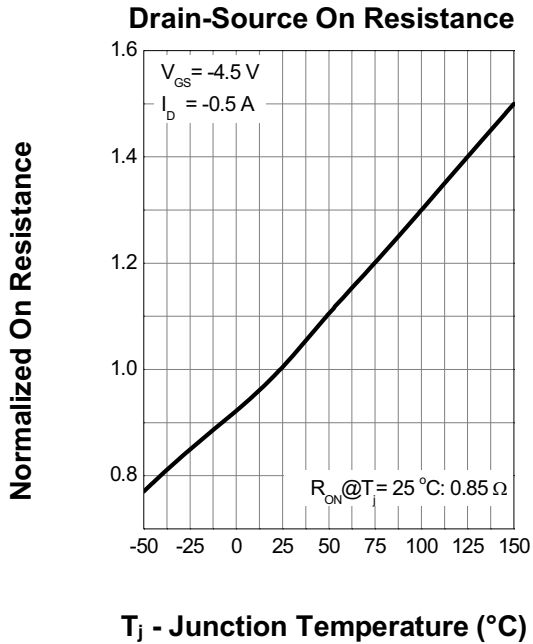
**Transfer Characteristics**



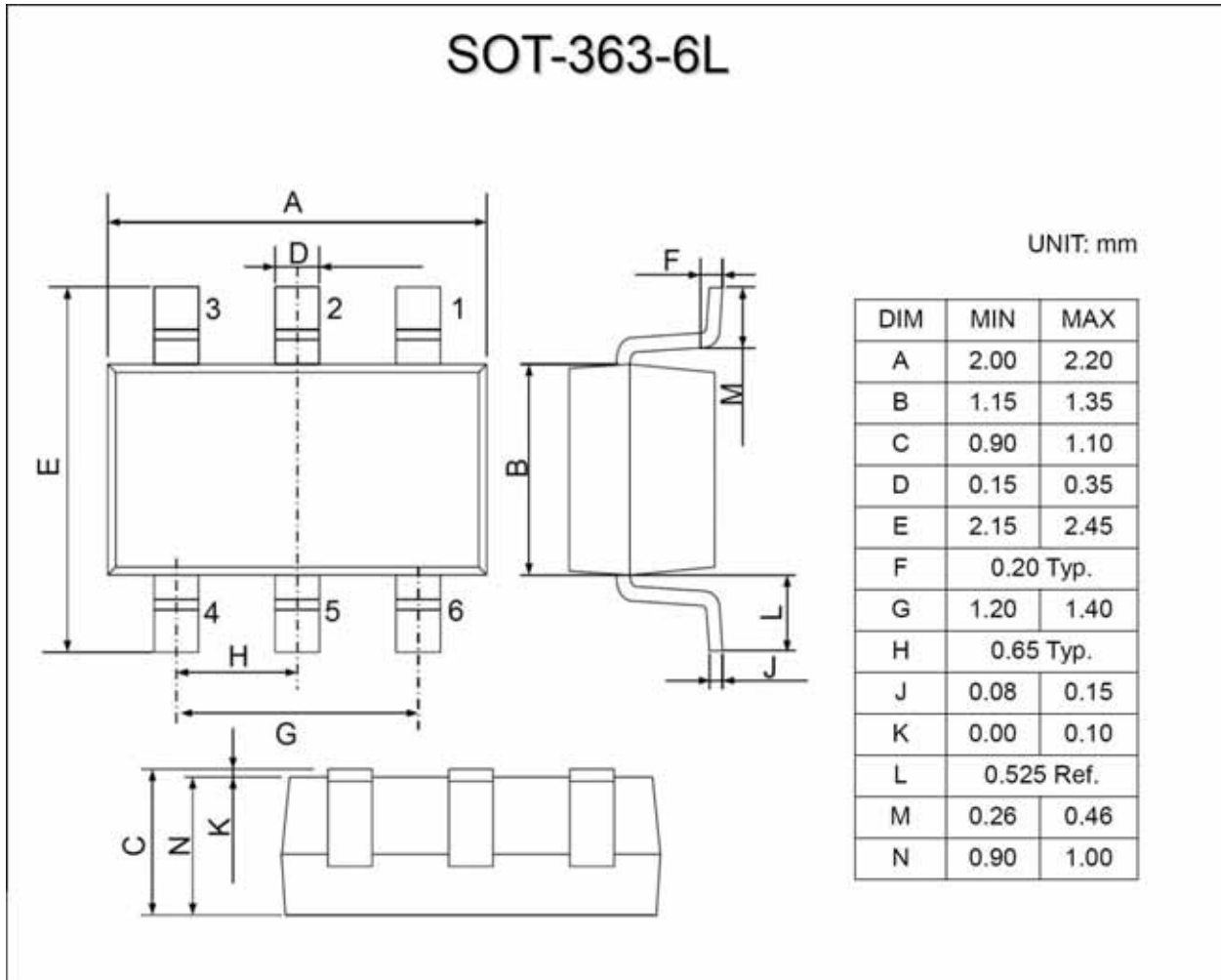
**Gate Threshold Voltage**



## P-Typical Characteristics



## Package Dimensions



PKG	tape	Reel	Box	pcs/reel	reel/box	pcs/box	box/carton	pcs/carton
SOT-363	IC-ZD-05	7" (IC-JP-05)	SOT363	3000	10	30000	4	120000



## DISCLAIMER NOTICE

Rectron Inc reserves the right to make changes without notice to any product specification herein, to make corrections, modifications, enhancements or other changes. Rectron Inc or anyone on its behalf assumes no responsibility or liability for any errors or inaccuracies. Data sheet specifications and its information contained are intended to provide a product description only. "Typical" parameters which may be included on RECTRON data sheets and/ or specifications can and do vary in different applications and actual performance may vary over time. Rectron Inc does not assume any liability arising out of the application or use of any product or circuit.

Rectron products are not designed, intended or authorized for use in medical, life-saving implant or other applications intended for life-sustaining or other related applications where a failure or malfunction of component or circuitry may directly or indirectly cause injury or threaten a life without expressed written approval of Rectron Inc. Customers using or selling Rectron components for use in such applications do so at their own risk and shall agree to fully indemnify Rectron Inc and its subsidiaries harmless against all claims, damages and expenditures.