

C1210C684K5PACTU

Aliases (C1210C684K5PAC7800)

SMD Comm X5R, Ceramic, 0.68 uF, 10%, 50 VDC, X5R, SMD, MLCC, Temperature Stable, Class II, 1210



Click [here](#) for the 3D model.

Dimensions	
Chip Size	1210
L	3.2mm +/-0.2mm
W	2.5mm +/-0.2mm
T	0.95mm +/-0.10mm
B	0.5mm +/-0.25mm

Packaging Specifications	
Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	4000

General Information	
Series	SMD Comm X5R
Style	SMD Chip
Description	SMD, MLCC, Temperature Stable, Class II
Features	Temperature Stable, Class II
RoHS	Yes
Termination	Tin
Marking	No
AEC-Q200	No
Component Weight	50 mg
Shelf Life	78 Weeks
MSL	1

Specifications	
Capacitance	0.68 uF
Measurement Condition	1 kHz 1.0Vrms
Capacitance Tolerance	10%
Voltage DC	50 VDC
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+85°C
Temperature Coefficient	X5R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	5% 1kHz 1.0Vrms
Aging Rate	5% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	735.3 MOhms

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