

**PCN Number: RC130803B (Revised 11/05/14)**    **Notification Date\*:** March 28, 2013  
**See Changes in Blue Text vs. Revised RC130803A (06/20/14)**

<b>Title:</b> : AT91SAM9G45 Migration to Revision C									
<b>Product Identification:</b> Ordering code and top side marking									
<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 50%;">MRL B Ordering Code</th> <th style="width: 50%;">MRL C Ordering Code</th> </tr> </thead> <tbody> <tr> <td><b>AT91SAM9G45B-CU*</b></td> <td>AT91SAM9G45C-CU</td> </tr> <tr> <td><b>AT91SAM9G45B-CU-999*</b></td> <td>AT91SAM9G45C-CU-999</td> </tr> </tbody> </table>		MRL B Ordering Code	MRL C Ordering Code	<b>AT91SAM9G45B-CU*</b>	AT91SAM9G45C-CU	<b>AT91SAM9G45B-CU-999*</b>	AT91SAM9G45C-CU-999		
MRL B Ordering Code	MRL C Ordering Code								
<b>AT91SAM9G45B-CU*</b>	AT91SAM9G45C-CU								
<b>AT91SAM9G45B-CU-999*</b>	AT91SAM9G45C-CU-999								
<b>*EOL is CANCELLED; both MRL B and MRL C will be maintained in production</b>									
<b>Reason for Change:</b>	<input type="checkbox"/> Material / Composition <input type="checkbox"/> Processing / Manufacturing	<input checked="" type="checkbox"/> Design / Firmware <input type="checkbox"/> Logistics	<input type="checkbox"/> Manufacturing Location <input type="checkbox"/> Quality / Reliability						
<b>Change Description:</b> A detailed description of the differences between the marketing revision level (MRL) B and C can be found in the data sheet. In summary, changes are limited to the correction of the following errata:									
<ul style="list-style-type: none"> <li>• Boot ROM: Boot issue on AT45-series SPI dataflash (section 49.2.1.2 )</li> <li>• UPHPS/UDPHS: Bad Lock of the USB High speed transceiver DLL. ( section 49.2.9.2)</li> </ul>									
<p>To increase manufacturing flexibility and ensure a long-term continuity of supply, Atmel will manufacture the <b>AT91SAM9G45C-CU</b> and <b>AT91SAM9G45C-CU-999</b> using both gold (Au) and copper bond (Cu) wires.            Atmel reserves the right to ship devices with either gold or copper bond wires.            The change will not impact reliability, soldering profiles or electrical characteristics.</p> <p>Revised due to change: Boot on SDCard is not functional on MRL C</p> <p>New Errata introduced and limited only to MRL C</p> <p>49.3.1 Boot ROM (Limited to MRL C)            49.3.1.1 Boot ROM: SDCARD Boot is not functional on MRL C            The SDcard boot functionality does not work due to an issue in the ROM Code.</p> <p>Problem Fix/Workaround  <b>The SD card boot functionality can be restored by connecting EBI_D0 and EBI_D6 via 10 KΩ resistors:</b>  <b>1. EBI_D0 should be connected to ground.</b>  <b>2. EBI_D6 should be connected to VDDIOM.</b></p>									
<b>Identification Method to Distinguish Change:</b> See section Product identification above.									
<b>Qualification Data:</b>	<input checked="" type="checkbox"/> Available	<input type="checkbox"/> Will be available (mm/dd/yr):	<input type="checkbox"/> Not Applicable						

<b>Samples:</b>	<input checked="" type="checkbox"/> Available	<input type="checkbox"/> Will be available (mm/dd/yr):	<input type="checkbox"/> Not Applicable
<b>Quantifiable Impact on Quality &amp; Reliability:</b> N/A			
<b>Forecasted Availability Date: Now</b> <b>Last Time Buy Date: Cancelled</b> <b>Last Ship Date: Cancelled</b>			
<i>*All orders placed after the notification date are non-cancellable and non-returnable (NCNR).</i>			
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