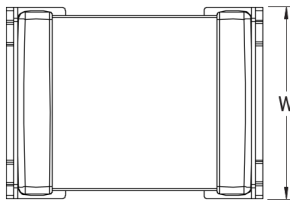
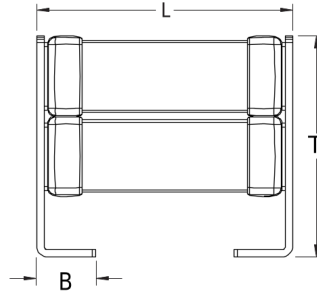


TOP VIEW  
Single or Double Chip Stack



SIDE VIEW  
Double Chip Stack



Click [here](#) for the 3D model.

### Dimensions

L	5mm +/-0.5mm
W	3.5mm +/-0.5mm
T	5mm +/-0.5mm
B	1.1mm +/-0.3mm

### Packaging Specifications

Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	400

### General Information

Series	KPS SMD Comm X7R
Style	Stacked Chip
Description	SMD, MLCC, Stacked, Double Chip, Temperature Stable
Features	Temperature Stable
RoHS	Yes
Termination	Tin
AEC-Q200	No
Component Weight	310 mg
Chip Size	1812-2
Shelf Life	78 Weeks
MSL	1

### Specifications

Capacitance	22 uF
Measurement Condition	120 Hz 0.5Vrms
Capacitance Tolerance	20%
Voltage DC	25 VDC
Dielectric Withstanding Voltage	62.5 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 120Hz 0.5Vrms
Dissipation Factor	3.5% 120 Hz 0.5Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	22.7 MOhms