

**CHANGE NOTIFICATION**

NOW PART OF



Analog Devices, Inc.  
 1630 McCarthy Blvd., Milpitas CA  
 (408) 432-1900

September 18, 2017

PCN#091817

Dear Sir/Madam:

**Subject: Notification of Change to LTC3246 Datasheet**

Please be advised that Analog Devices, Inc. Milpitas, California has made a minor change to the LTC3246 product datasheet to facilitate improvement in our manufacturing capability. The changes are shown on the attached pages of the marked up datasheet. There was no change in form, fit, function, quality or reliability of the product. The product shipped after November 18, 2017 will be tested to the new limits.

Should you have any questions or concerns please contact your local Analog Devices sales representatives or you may contact me at 408-432-1900 ext. 2077, or by e-mail at [JASON.HU@ANALOG.COM](mailto:JASON.HU@ANALOG.COM). If I do not hear from you by November 18, 2017, we will consider this change to be approved by your company.

Sincerely,

Jason Hu  
 Quality Assurance Engineer

**For questions on this PCN, please contact Jason Hu or you may send an email to your regional contacts below or contact your local ADI sales representatives.**

<b>Americas:</b> PCN_Americas@analog.com	<b>Europe:</b> PCN_Europe@analog.com	<b>Japan:</b> PCN_Japan@analog.com
		<b>Rest of Asia:</b> PCN_ROA@analog.com

## LTC3246

## ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the specified operating junction temperature range, otherwise specifications are at  $T_A = 25^\circ\text{C}$ .  $V_{IN} = 12\text{V}$ ,  $C_{FLY} = 2.2\mu\text{F}$ ,  $C_{OUT} = 10\mu\text{F}$ , unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS	
$V_{IN}$	Operating Input Voltage Range	(Note 5)	● 2.7		38	V	
$V_{UVLO}$	$V_{IN}$ Undervoltage Lockout Threshold		●	2.35	2.7	V	
$I_{VIN}$	$V_{IN}$ Quiescent Current Shutdown CP Enabled, Output in Regulation	SEL1 = SEL2 = 0V SEL1 = $V_{IN}$ and/or SEL2 = $V_{IN}$ , RST1 = 5V		1.5 20	3 30	$\mu\text{A}$ $\mu\text{A}$	
$V_{HIGH}$	SEL1, SEL2 Input Voltage		●	1.1	1.6	V	
$V_{LOW}$	SEL1, SEL2 Input Voltage		● 0.4	0.8		V	
$I_{LOW}$	SEL1, SEL2 Input Current	$V_{PIN} = 0\text{V}$	● -1	0	1	$\mu\text{A}$	
$I_{HIGH}$	SEL1, SEL2 Input Current	$V_{PIN} = 38\text{V}$	● 0.5	1	2	$\mu\text{A}$	
<b>Charge Pump Operation</b>							
$V_{OUTS\_5}$	VOOTS/ADJ Regulation Voltage SEL1 = 0V, SEL2 = $V_{IN}$	$2.7\text{V} < V_{IN} < 38\text{V}$ (Notes 5, 6)	● 4.8		5.2	V	
$V_{OUTS\_3}$	VOOTS/ADJ Regulation Voltage SEL1 = $V_{IN}$ , SEL2 = $V_{IN}$	$2.7\text{V} < V_{IN} < 38\text{V}$ (Notes 5, 6)	● 3.17		3.43	V	
$V_{ADJ}$	VOOTS/ADJ Regulation Voltage SEL1 = $V_{IN}$ , SEL2 = 0V	$2.7\text{V} < V_{IN} < 38\text{V}$ (Notes 5, 6)	● 1.08	1.11	1.14	V	
$I_{ADJ}$	VOOTS/ADJ Input Current SEL1 = SEL2 = $V_{IN}$		● -50	0	+50	nA	
$I_{OUT\_SCKT}$	$I_{OUT}$ Short Circuit Foldback Current	$V_{OUT} = 0\text{V}$		250		mA	
$R_{OUT}$	Charge Pump Output Impedance	2:1 Step-Down Mode 1:1 Step-Down Mode, $V_{IN} = 6\text{V}$ ← 5.5V 1:2 Step-Up Mode, $V_{IN} = 3\text{V}$ , $V_{OUT} \geq 3.3\text{V}$ (Note 6)	●	1 1.2 4	8	$\Omega$ $\Omega$ $\Omega$	
$V_{OUT\_OV\_RST}$	$V_{OUT}$ Overvoltage Reset	% of Final Regulation Voltage at Which $V_{OUT}$ Rising Makes RST Go Low $V_{OUT}$ Falling Makes RST Go Hi-Z	● ●	106 109 108.5	111.5	% %	
$V_{OUT\_UV\_RST}$	$V_{OUT}$ Undervoltage Reset	% of Final Regulation Voltage at Which $V_{OUT}$ Rising Makes RST Go Hi-Z $V_{OUT}$ Falling Makes RST Go Low	● ●	93 97.5 95	99	% %	
$V_{OUT\_PD}$	$V_{OUT}$ Pull-Down in Shut Down	SEL1 = SEL2 = 0V		100		k $\Omega$	
$V_{OUT\_RIPPLE}$	$V_{OUT}$ Ripple Voltage	$C_{OUT} = 10\mu\text{F}$ $C_{OUT} = 22\mu\text{F}$		50 25		mV mV	
<b>Reset Timer Control Pin (RT)</b>							
$I_{RT(UP)}$	RT Pull-Up Current	$V_{RT} = 0.3\text{V}$	● -2	-3.1	-4.2	$\mu\text{A}$	
$I_{RT(DOWN)}$	RT Pull-Down Current	$V_{RT} = 1.3\text{V}$	● 2	3.1	4.2	$\mu\text{A}$	
$I_{RT(INT)}$	Internal RT Detect Current	$V_{RT} = V_{BIAS}$	●	0.4	1	$\mu\text{A}$	
$V_{RT(INT)}$	RT Internal Timer Threshold	$V_{RT}$ Rising	● 2.0	2.4	2.65	V	
<b>Reset Timer Input (RST1)</b>							
$V_{RST1\_H}$	RST1 Input High Voltage		●	1.22	1.27	V	
$V_{RST1\_L}$	RST1 Input Low Voltage		● 1.04	1.05	1.2	V	
$I_{RST1\_H}$	RST1 Input High Current	RST1 = 5V	● -1	0	1	$\mu\text{A}$	
$I_{RST1\_L}$	RST1 Input Low Current	RST1 = 0V	● -1	0	1	$\mu\text{A}$	
<b>Reset Timing</b>							
$t_{RST(INT)}$	Internal Reset Timeout Period	$V_{RT} = V_{BIAS}$		150	200	270	ms
$t_{RST(EXT)}$	Adjustable Reset Timeout Period	$C_{RT} = 2.2\text{nF}$	● 14	21	28	ms	
$t_{RSTIL}$	RST1 Low to RST Asserted		● 5	20	40	$\mu\text{s}$	

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For more information [www.linear.com/LTC3246](http://www.linear.com/LTC3246)

3