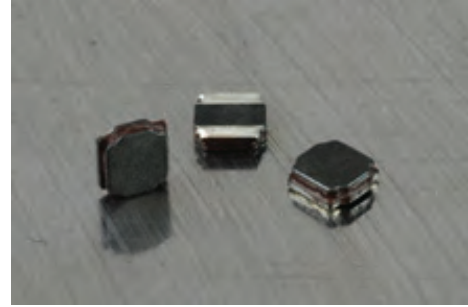
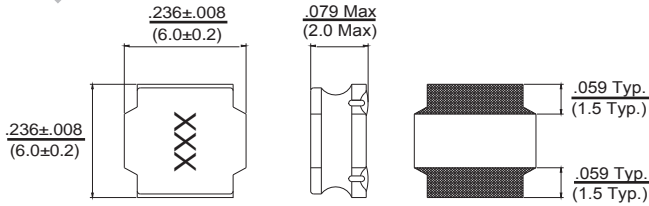




SMD SHIELDED POWER CHIP INDUCTOR

PCSV60

Dimensions:  $\frac{\text{Inches}}{\text{(mm)}}$

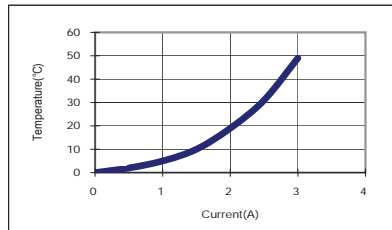


Allied Part Number	Inductance ( $\mu\text{h}$ )	Tolerance (%) *	Test Freq. KHz, 1V	RDC m( $\Omega$ ) $\pm 30\%$	Isat (A) Max. Typ.	Irms (A) Typ.
PCSV60-R50_-RC	0.5	T	100	13	8.0	5.3
PCSV60-R90_-RC	0.9	T	100	18	6.3	4.2
PCSV60-1R0_-RC	1.0	T	100	19	6.2	4.1
PCSV60-1R5_-RC	1.5	M, T	100	26	5.0	3.6
PCSV60-2R2_-RC	2.2	M, T	100	34	4.2	3.2
PCSV60-3R3_-RC	3.3	M, T	100	40	3.2	2.7
PCSV60-4R7_-RC	4.7	M, T	100	58	2.5	2.2
PCSV60-6R8_-RC	6.8	M, T	100	85	2.2	1.8
PCSV60-100_-RC	10	M, T	100	125	2.0	1.6
PCSV60-150_-RC	15	M, T	100	190	1.3	1.3
PCSV60-220_-RC	22	M, T	100	260	1.1	1.1

\*Enter desired tolerance: M= $\pm 20\%$ , T= $\pm 30\%$   
All specifications subject to change without notice.

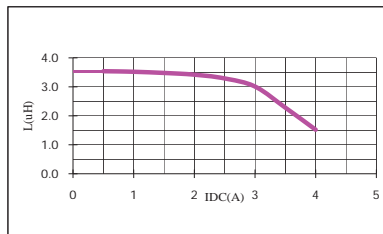
Temp vs Current Graph for 3.3 $\mu\text{h}$

IDC(A)	$\Delta T(^{\circ}\text{C})$	surface temp( $^{\circ}\text{C}$ )	time(minute)
0.0	0	25	30
0.5	2	27	30
1.0	5	30	30
1.5	10	35	30
2.0	19	44	30
2.5	31	56	30
3.0	49	74	30



Inductance vs Current Graph for 3.3 $\mu\text{h}$

IDC(A)	L( $\mu\text{h}$ )
0.0	3.55
0.5	3.54
1.0	3.52
1.5	3.48
2.0	3.42
2.5	3.29
3.0	3.01
3.5	2.28
4.0	1.52



**Features**

- Magnetically Shielded Construction
- Low Profile

**Electrical**

**Inductance Range:** 0.5 $\mu\text{h}$  - 22 $\mu\text{h}$  other values available

**Tolerance:** 20%

**Operating Temp:** -25 $^{\circ}\text{C}$  ~ +105 $^{\circ}\text{C}$

**Isat:** Based on Inductance drop less than 30%

**Irms:** Based on a temp rise of 40 $^{\circ}\text{C}$  typical

**Resistance to Soldering Heat**

Pre-Heat 150 $^{\circ}\text{C}$ , 1 Min.

**Solder Composition:** Sn/Ag3.0/Cu0.5

**Solder Temp:** 260 $^{\circ}\text{C}$  +/- 5 $^{\circ}\text{C}$

**Immersion time:** 10 sec. +/- 1 sec.

**Test Equipment**

**(L):** HP4284A

**(RDC):** Chroma MilliOhm Meter 16502

**Current:** HP4284A + HP42841A

**Physical**

**Packaging:** Tape and reel

**Marking:** EIA Inductance Code.

**Land Pattern**

