



June 13, 2016

Subject: PCN# 09A-16 Notification of Changes to the L-ASC10 and Platform Manager 2 Data Sheets

Dear Lattice Customer,

Lattice Semiconductor is providing this notification of changes to the L-ASC10 and the Platform Manager™ 2 Data Sheets.

Change Description

The new L-ASC10 Data Sheet (DS1042 Version 01.6 released in June 2016) and Platform Manager 2 Data Sheet (DS1043 Version 01.4, released in June 2016) updates the allowed I²C Measurement Read Sequences for the Internal Temperature Monitor (TMON_INT) reading and Temperature Monitor (TMON) status reading.

Specifically, any READ_MEAS_CTRL (0x52) command which reads addresses 0x85 or 0x86 as the final data byte must be followed by an additional READ_MEAS_CTRL (0x52) command of a different address (such as 0x70) before issuing any other I²C read command.

Only system designs that immediately issue I²C commands other than READ_MEAS_CTRL (0x52) after the reading of the restricted measurement registers are affected by this PCN. It is anticipated that most applications are not impacted by this specification change.

There is no change to any device design or Lattice Diamond™ software.

Affected Products

The Ordering Part Numbers (OPNs) affected by this PCN are as follows:

L-ASC10-1SG48I

LPTM21-1AFTG237C

LPTM21-1AFTG237I

Note: This PCN also affects any custom devices (i.e. factory programmed, special test, etc.) which are derived from any of the devices listed above.

Datasheet Specifications

The updated L-ASC10 Data Sheet (DS1042 Version 01.6, released in June 2016) and Platform Manager 2 Data Sheet (DS1043 Version 01.4, released in June 2016) reflect the restriction described above and are available on the Lattice website.

PCN Timing

The datasheet changes are effective immediately and retroactively. There are no changes to the silicon and therefore samples are not applicable to these data sheet changes.

Lattice Semiconductor Home Page: <http://www.latticesemi.com>

Recommended Customer Action

Those customers with designs that immediately issue I²C commands other than READ_MEAS_CTRL (0x52) after the reading of the restricted measurement registers are required to update their system behavior (I²C master read sequences) as described in the revised datasheet to address the new restriction.

Customers who have further questions regarding this specification change are encouraged to contact local field support or at sales@latticesemi.com. It is not necessary to recompile the Lattice Diamond design in response to this notification.

Sincerely,

Lattice PCN Administration

Lattice Semiconductor Home Page: <http://www.latticesemi.com>