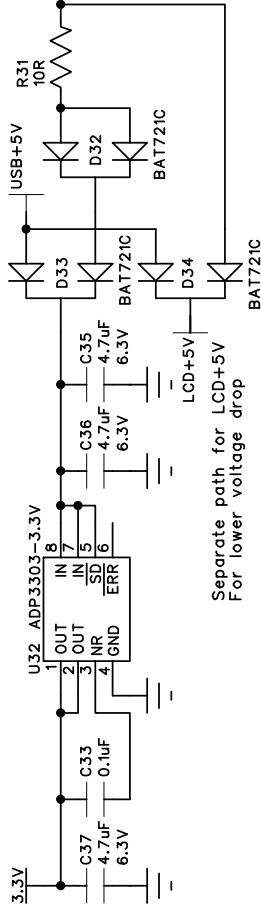
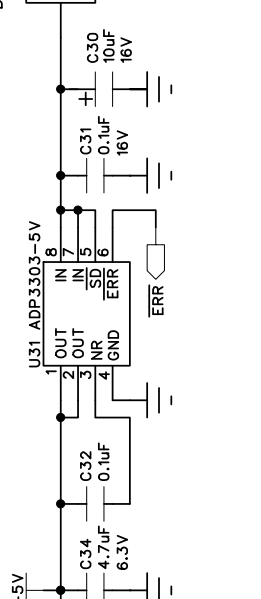


### DVDD 3.3V REGULATOR

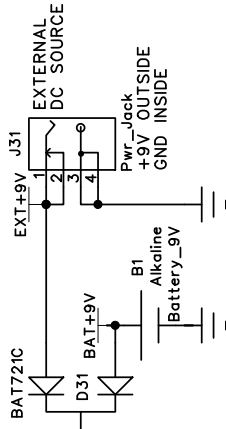
DVDD auto selection.  
The extra diode makes USB+5V  
the preferred source for uC 3.3V



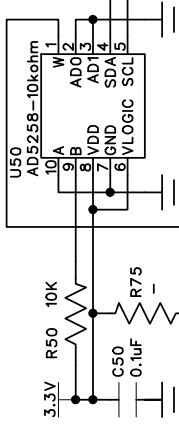
### AVDD 5.0V REGULATOR



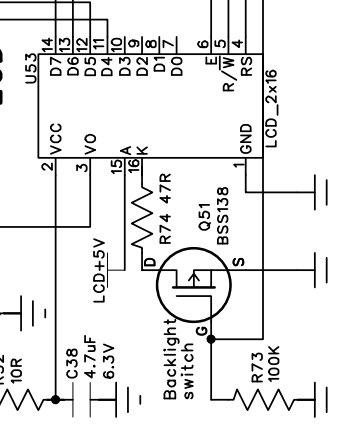
### 9V DC SOURCE



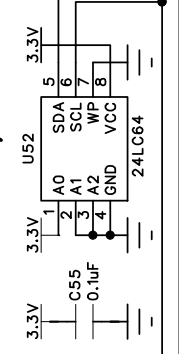
### LCD CONTRAST



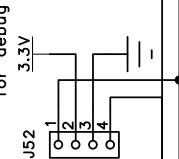
### LCD



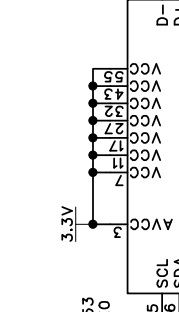
### EEPROM-SW/USB ID



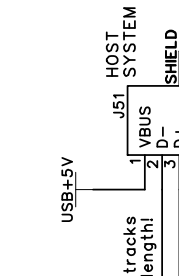
### I2C connector for debug



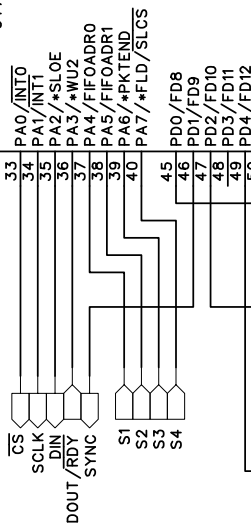
### USB CONTROLLER



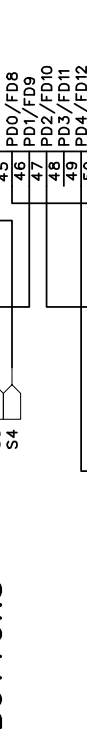
### USB INTERFACE



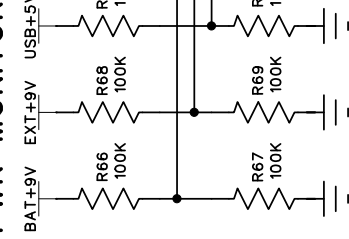
### ADC SERIAL INTERFACE



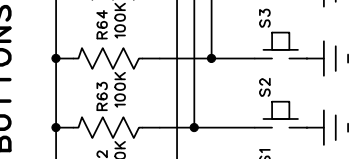
### BUTTONS



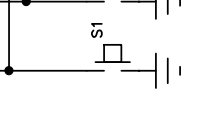
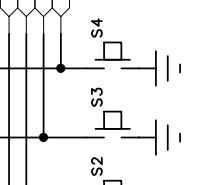
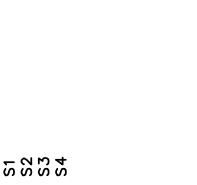
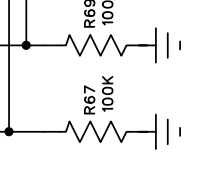
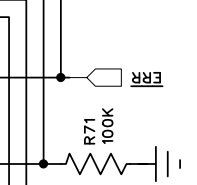
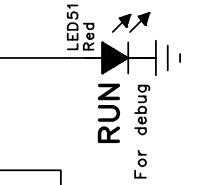
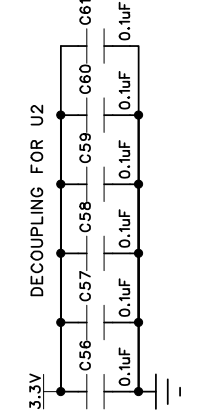
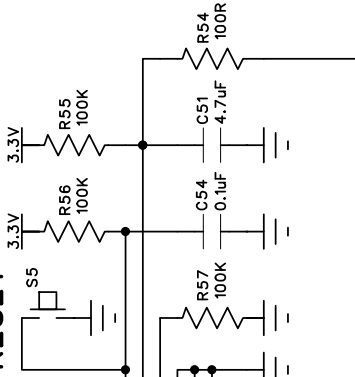
### PWR MONITOR

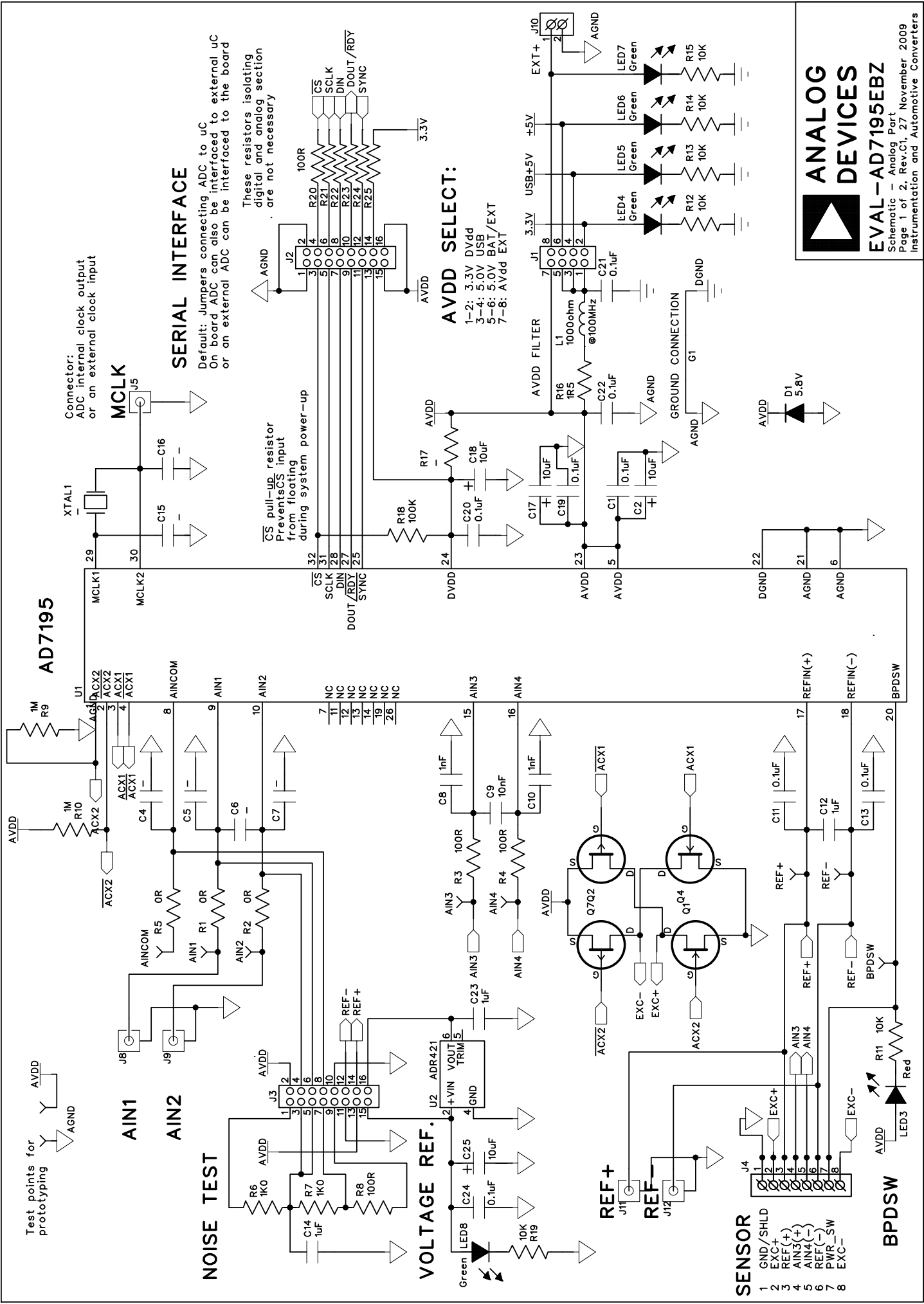


### BUTTONS



### RESET





**AD7195**

Connector:  
 ADC internal clock output  
 or an external clock input

**MCLK**

**SERIAL INTERFACE**

Default: Jumpers connecting ADC to uC  
 On board ADC can also be interfaced to external uC  
 or an external ADC can be interfaced to the board

These resistors isolating  
 digital and analog section  
 are not necessary

CS pull-up resistor  
 Prevents CS input  
 from floating  
 during system power-up

**AVDD SELECT:**

- 1-2: 3.3V DVdd
- 3-4: 5.0V USB
- 5-6: 5.0V BAT/EXT
- 7-8: AVdd EXT

**SENSOR**

- 1 GND/SHLD
- 2 EXC+
- 3 REF(+)
- 4 AIN3(+)
- 5 AIN4(-)
- 6 REF(-)
- 7 PWR\_SW
- 8 EXC-

**BPDSW**

D1 5.8V