

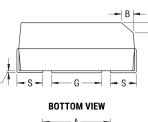
## T493D476K025AK6110

T493 HRA, Tantalum, MnO2 Tantalum, HRA, 47 uF, 10%, 25 VDC, SMD, MnO2, Molded, High Reliability, N/A, 700 mOhms, 7343, Height Max = 3.1mm

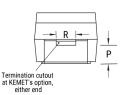
CATHODE (-) END VIEW

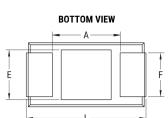


ANODE (+) END VIEW



SIDE VIEW





General Information		
Series	T493 HRA	
Dielectric	MnO2 Tantalum	
Style	SMD Chip	
Description	SMD, MnO2, Molded, High Reliability	
Features	High Reliability	
RoHS	No	
Prop 65	A WARNING: Cancer and reproductive harm - http://www.p65warnings.ca.gov.	
SCIP Number	656db0b8-8445-4117-82f2-2f12e764f0b1	
Termination	Solder Fused	
AEC-Q200	No	
Component Weight	412.33 mg	
Notes	P and R dimensions represents the minimum solderable area of the termination surface entirely below cutout (if one is present).	

Click here for the 3D model.

Dimensions	
Footprint	7343
L	7.3mm +/-0.3mm
W	4.3mm +/-0.3mm
Н	2.8mm +/-0.3mm
Т	0.13mm REF
S	1.3mm +/-0.3mm
F	2.4mm +/-0.1mm
А	3.8mm MIN
В	0.5mm +/-0.15mm
E	3.5mm REF
G	3.5mm REF
Р	0.5mm MIN
R	1mm REF
Х	0.1mm +/-0.1mm

Т

Specifications	
Capacitance	47 uF
Capacitance Tolerance	10%
Voltage DC	25 VDC (85C), 16.75 VDC (125C)
Temperature Range	-55/+125°C
Rated Temperature	85°C
Dissipation Factor	10% 120Hz 25C
Failure Rate	N/A
Resistance	0.7 Ohms (100kHz 25C)
Ripple Current	463 mA (rms, 100kHz 25C)
Leakage Current	11.8 uA (5min 25°C)

Testing and Reliability	Standard Testing Only
resting and reclaping	otariaara resting only

Packaging Specifications		
Packaging	T&R, 178mm	
Packaging Quantity	500	

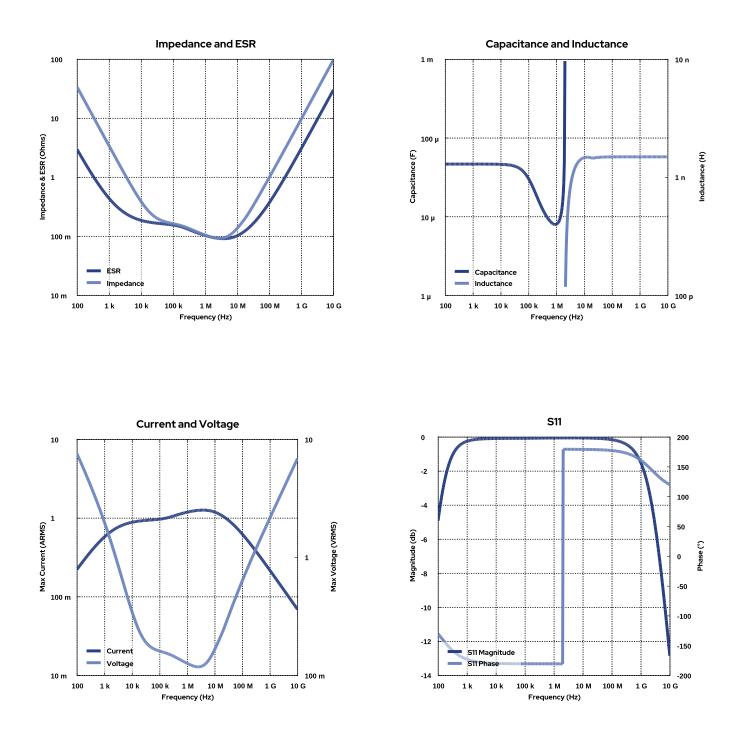
Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.



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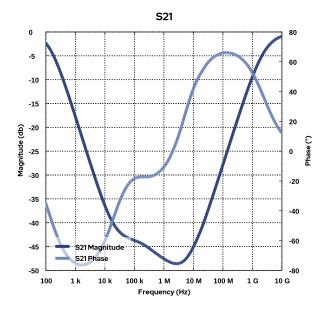
## Simulations

For the complete simulation environment please visit K-SIM.





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## These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.
- The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
- The effects shown herein are based on measured data from a multiple part sample of the parts in question.
- Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance. The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages generated at any other
- harmonics.
- Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.