



INPUT CURRENT LIMIT SETTINGS

D0	D1	D2	CURRENT LIMIT	CHARGER STATUS
0	0	0	100mA (1X)	ON
0	0	1	100mA (1X)	OFF
0	1	0	500mA (5X)	ON
0	1	1	500mA (5X)	OFF
1	0	0	1A (10X)	ON
1	0	1	1A (10X)	OFF
1	1	0	500uA (SUSP)	OFF
1	1	1	2.5mA (SUSP)	OFF

NOTE: UNLESS OTHERWISE NOTED, ALL CAPACITORS AND RESISTORS ARE 0402, ALL RESISTORS ARE 1%

<h3>CUSTOMER NOTICE</h3> <p>LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.</p>		CONTRACT NO.		<p>1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 Fax: (408)434-0507 LTC Confidential-For Customer Use Only</p>	
		APPROVALS			
DRAWN: M. Merchant		CHECKED:		High Efficiency Battery Charger/USB Power Manager	
APPROVED:		ENGINEER: M. Merchant		SIZE A	
DESIGNER:		DWG NO. DC1377A		REV A	
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		DATE: Monday, May 05, 2008		SHEET 1 OF 1	