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| Company | Trenz Electronic GmbH |
| PCN Number | PCN-20200511 |
| Title | TE0807-02 to TE0807-03 Hardware Revision Change |
| Subject | Hardware Revision Change |
| Issue Date | 2020-06-05 |

Products Affected

This change affects all Trenz Electronic TE0807 SoMs of revision 02: TE0807-02-*

| Affected Product | Replacement |
|------------------|-------------|
| TE0807-02-* | TE0807-03-* |

Changes

#1 . Separated VREFP pin reference voltage from PL_1V8 Rail

Type: SCH change

Reason: Supply of VrefP pin was out of recommended operation condition.

Impact: Xilinx specification fulfilled. PL XADC works properly (AVN-20200319 is obsolete).

#2 Added signal BG1, revised routing and placement of DDR4

Type: Schematic change

Reason: Add support of new packages of DDP DDR4 ICs.

Impact: DDP DDR4 now supported.

#3 R5 value changed to 49.9 Ohm and connected to VCCO

Type: Schematic change

Reason: Xilinx recommendation UG583.

Impact: DDR4-Alert signal pulled up to VCCO_PSDDR.

#4 Added testpoints

Type: PCB

Reason: Improve for better automatic test routines.

Impact: None

#5 Full update from Libraries

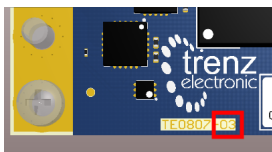
Type: PCB

Reason: Improve production yield

Impact: None

Method of Identification

The revision number is printed in the top side of the PCB.



Production Shipment Schedule

Date is not yet fixed and depends on customer needs.

Contact Information

If you have any questions related to this PCN, please contact Trenz Electronics Technical Support at

- forum.trenz-electronic.de
- wiki.trenz-electronic.de
- support@trenz-electronic.de (subject = PCN-20200511)
- phone
 - national calls: 05741 3200-0
 - international calls: 0049 5741 3200-0

Disclaimer

Any projected dates in this PCN are based on the most current product information at the time this PCN is being issued, but they may change due to unforeseen circumstances. For the latest schedule and any other information, please contact your local Trenz Electronic sales office, technical support or local distributor.

This PCN follows JEDEC Standard J-STD-046.