

<b>PCN Number:</b>	20190214001.0	<b>PCN Date:</b>	February 19, 2019
<b>Title:</b>	Datasheet for LMX2572		
<b>Customer Contact:</b>	<a href="#">PCN Manager</a>	<b>Dept:</b>	Quality Services
<b>Change Type:</b>			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process

### Notification Details

#### Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



**LMX2572**

SNAS740B –OCTOBER 2017–REVISED JANUARY 2019

#### Changes from Revision A (October 2017) to Revision B

**Page**

• Changed RampDir pin description from: ...ramp size selection... to: ...ramp segment selection...	4
• Changed RFoutAM pin description from: High impedance... to: Low impedance...	4
• Changed RFoutAP pin description from: High impedance... to: Low impedance...	4
• Changed RFoutBM pin description from: High impedance... to: Low impedance...	4
• Changed RFoutBP pin description from: High impedance... to: Low impedance...	4
• Changed VbiasVCO pin decoupling capacitor requirement	5
• Changed VbiasVCO2 pin decoupling capacitor requirement	5
• Changed VccMASH pin decoupling capacitor requirement	5
• Changed VccVCO pin decoupling capacitor requirement	5
• Changed VccVCO2 pin decoupling capacitor requirement	5
• Changed VregVCO pin decoupling capacitor requirement	5
• Added Vtune pin shunt capacitor requirement	5
• Changed V <sub>OH</sub> and V <sub>OL</sub> data in Electrical Characteristics	8
• Changed SCK to CSB low time symbol	8
• Changed <a href="#">Figure 28</a>	13
• Added charge pump gain table	16
• Deleted sentence 'When the device comes out of the powered down state, either by resuming the POWERDOWN bit to zero or by pulling back CE pin HIGH (if it was powered down by CE pin), it is required that register R0 with FCAL_EN = 1 be programmed again to re-calibrate the device.' from the <i>Powerdown</i> section	18
• Added sentence 'The wake-up time for the device to come out of the powered state is adjustable.' to the <i>Powerdown</i> section	18
• Changed <i>Programming Sequence</i> step from: Wait 100 μs... to: Wait 500 μs...	21
• Changed R6 initial programming from: No to: Depends	21
• Changed R52 initial programming from: No to: Yes	22

• Added LDO_DLY in register R6 .....	24
• Changed R15[0] from: 1 to: 0 .....	24
• Changed R15 POR value .....	24
• Changed R20[14] from: 0 to: 1 .....	24
• Changed R52[0] from: 0 to: 1 .....	25
• Changed register R0, FCAL_HPFD_ADJ value definition .....	28
• Added sentence 'Writing 0 to this field is prohibited.' to FCAL_EN bit description .....	28
• Added LDO_DLY in register R6 .....	30
• Changed MULT bit description from: ...30 MHz... to: ...40 MHz... .....	32
• Changed register R14 default value .....	33
• Changed R15[0] from: 1 to: 0 .....	33
• Changed register R20 default value .....	35
• Deleted VCO_SEL_STRT_EN = 1 in Register 20 Field Descriptions .....	35
• Changed register R46, OUTB_MUX value definition .....	42
• Changed register R52 programming value .....	44
• Changed register R58, INPIN_LVL value definition .....	45
• Changed from: MASH reset count... to: This register... .....	48
• Changed VCO_CAPCTRL_STRT reset value .....	51
• Changed register R114, FSK_MODE_SEL value definition .....	61
• Changed Figure 165 and Figure 166 .....	65
• Changed setup procedure step from: ...divide N... to: ...set N = N' / 2... .....	68
• Changed RAMP_MODE = 1 to RAMP_MANUAL = 1 in the <i>Manual Ramping Mode</i> section .....	69
• Changed RAMP_THRESH value suggestion .....	69
• Deleted paragraph 'For ramping that are not calibration free, the ramp waveform is more like a staircase ramp. For all automatic ramping waveforms, be aware that there is a very small phase disturbance as the VCO crosses over the integer boundary, so one might consider using the input Multiplier to avoid these or timing the VCO calibration at integer boundaries.' from the <i>Automatic Ramping Mode</i> section .....	70
• Changed ADR_HOLD = 1 to ADD_HOLD = 1 .....	74
• Added an application section for external loop filter .....	75
• Added an application section for powerup wake up time .....	75

The datasheet number will be changing.

Device Family	Change From:	Change To:
LMX2572	SNAS740A	SNAS740B

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/LMX2572>

**Reason for Change:**

To accurately reflect device characteristics.

**Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):**

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

**Changes to product identification resulting from this PCN:**

None.

**Product Affected:**

LMX2572RHAR	LMX2572RHAT		
-------------	-------------	--	--

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	<a href="mailto:PCNAmericasContact@list.ti.com">PCNAmericasContact@list.ti.com</a>
Europe	<a href="mailto:PCNEuropeContact@list.ti.com">PCNEuropeContact@list.ti.com</a>
Asia Pacific	<a href="mailto:PCNAsiaContact@list.ti.com">PCNAsiaContact@list.ti.com</a>
Japan	<a href="mailto:PCNJapanContact@list.ti.com">PCNJapanContact@list.ti.com</a>