

PCN Number:	20150903003	PCN Date:	09/10/2015
Title:	Datasheet for ADC12J4000		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
		<input type="checkbox"/>	Wafer Bump Site
		<input type="checkbox"/>	Wafer Bump Material
		<input type="checkbox"/>	Wafer Bump Process
		<input type="checkbox"/>	Wafer Fab Site
		<input type="checkbox"/>	Wafer Fab Materials
		<input type="checkbox"/>	Wafer Fab Process

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification etc.

The product datasheet(s) is being updated as summarized below.

The following change history provides further details.



ADC12J4000

SLAS989C – JANUARY 2014 – REVISED JULY 2015

Changes from Revision B (September 2014) to Revision C

Page

• Added additional voltage difference parameters to the <i>Absolute Maximum Ratings</i> table	8
• Added junction temperature to the <i>Absolute Maximum Ratings</i> table	8
• Added common mode voltage parameter to the <i>Recommended Operating Conditions</i> table. Changed CLK to SYSREF, and ~SYNC	9
• Changed the $f_S / 4 + F_{IN}$ spur MAX limit from –58.7 dBFS to –60 dBFS to align with the SFDR max limit of 60 dBFS	11
• Deleted the <i>Differential Analog Input Connection</i> image in <i>The Analog Inputs</i> section	27
• Added note about offset adjust in Background Calibration Mode to the <i>Offset Adjust</i> section and I/O offset register tables	31
• Added the <i>Calibration Cycle Timing for Different Calibration Modes and Options</i> table in the <i>Timing Calibration Mode</i> section	46
• Changed 0x004-0x005 to RESERVED in the <i>Standard SPI-3.0 Registers</i> summary table.....	56

The datasheet number will be changing.

Device Family	Change From:	Change To:
ADC12J4000	SLAS989B	SLAS989C

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/ADC12J4000>

Reason for Change:

To more accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:			
ADC12J4000NKE	ADC12J4000NKE10	ADC12J4000NKER	ADC12J4000NKET

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
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