

L1007C1R5MPWST

Aliases (L1007C1R5MPWST)

Not for New Design

KEMET, L-PWS, Power, 1.5 uH, 20%



Click [here](#) for the 3D model.

Dimensions

| | |
|-----------|----------------|
| Chip Size | 1007 |
| L | 2.5mm +/-0.2mm |
| W | 1.8mm +/-0.2mm |
| T | 1.8mm +/-0.2mm |
| LL | 0.5mm +/-0.2mm |
| B | 0.5mm +/-0.2mm |

Packaging Specifications

| | |
|--------------------|------|
| Packaging | T&R |
| Packaging Quantity | 2000 |

General Information

| | |
|-------------|--------------------------------|
| Series | L-PWS |
| Style | SMD Chip |
| Description | Surface Mount Inductor |
| Features | Standard Type |
| RoHS | Yes |
| Notes | Last Order Date June 30, 2023. |

Specifications

| | |
|-------------------------|-------------------|
| Inductance | 1.5 uH (7.96 MHz) |
| Inductance Tolerance | 20% |
| Temperature Range | -40/+105°C |
| Current | 400 mAmps (MAX) |
| DC Resistance | 0.07 Ohms |
| Self-Resonant Frequency | 80 MHz |

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