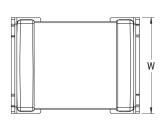


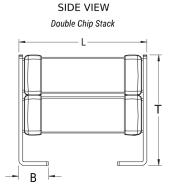
a YAGEO company

## C1210C105M1R2L7186

KPS SMD Comm X7R SnPb, Ceramic, 1 uF, 20%, 100 VDC, X7R, 1210-2

## TOP VIEW Single or Double Chip Stack





Click here for the 3D model.

Dimensions	
L	3.5mm +/-0.3mm
W	2.6mm +/-0.3mm
Т	6.15mm +/-0.15mm
В	0.8mm +/-0.15mm

Packaging Specifications	l
Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	300

General Information		
Series	KPS SMD Comm X7R SnPb	
Style	Stacked Chip	
Description	SMD, MLCC, Stacked, Double Chip, Temperature Stable	
Features	Temperature Stable	
RoHS	No	
Prop 65	▲ WARNING: Cancer and reproductive harm - http://www.p65warnings.ca.gov.	
SCIP Number	795b247e-78a9-405c-8728-223c2d9782a2	
Termination	Lead (SnPb)	
AEC-Q200	No	
Component Weight	290 mg	
Chip Size	1210-2	
Shelf Life	78 Weeks	
MSL	1	

Specifications	
Capacitance	1uF
Measurement Condition	1 kHz 1.0Vrms
Capacitance Tolerance	20%
Voltage DC	100 VDC
Dielectric Withstanding Voltage	250 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5% 1 kHz 1.0Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	500 MOhms

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