

<b>PCN Number:</b>	<b>20160117004</b>	<b>PCN Date:</b>	1/18/2016
<b>Title:</b>	Datasheet for ADC12J1600/ADC12J2700		
<b>Customer Contact:</b>	<a href="#">PCN Manager</a>	<b>Dept:</b>	Quality Services
<b>Change Type:</b>			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
		<input type="checkbox"/>	Wafer Bump Site
		<input type="checkbox"/>	Wafer Bump Material
		<input type="checkbox"/>	Wafer Bump Process
		<input type="checkbox"/>	Wafer Fab Site
		<input type="checkbox"/>	Wafer Fab Materials
		<input type="checkbox"/>	Wafer Fab Process

### Notification Details

#### Description of Change:

Texas Instruments Incorporated is announcing an information only notification etc.

The product datasheet(s) is being updated as summarized below.

The following change history provides further details.



**ADC12J1600, ADC12J2700**

SLAS969C – JANUARY 2014 – REVISED JULY 2015

#### Changes from Revision B (September 2014) to Revision C

Page

• Added additional voltage difference parameters to the <i>Absolute Maximum Ratings</i> table .....	8
• Added junction temperature to the <i>Absolute Maximum Ratings</i> table .....	8
• Added common mode voltage parameter to the <i>Recommended Operating Conditions</i> table. Changed CLK to SYSREF, and ~SYNC .....	9
• Changed some of the maximum interleaving offset values for both devices to tighten the levels .....	10
• Deleted the <i>Differential Analog Input Connection</i> image in <i>The Analog Inputs</i> section .....	31
• Added note about offset adjust in Background Calibration Mode to the <i>Offset Adjust</i> section and I/O offset register tables .....	35
• Added the <i>Calibration Cycle Timing for Different Calibration Modes and Options</i> table in the <i>Timing Calibration Mode</i> section .....	50
• Changed 0x004-0x005 to RESERVED in the <i>Standard SPI-3.0 Registers</i> summary table .....	60

The datasheet number will be changing.

Device Family	Change From:	Change To:
ADC12J1600/ ADC12J2700	SLAS969B	<b>SLAS969C</b>

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/ADC12J1600>

#### Reason for Change:

To more accurately reflect device characteristics.

#### Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

#### Changes to product identification resulting from this PCN:

None.

<b>Product Affected:</b>			
ADC12J1600NKE	ADC12J1600NKE10	ADC12J1600NKER	ADC12J1600NKET
ADC12J2700NKE	ADC12J2700NKE10	ADC12J2700NKER	ADC12J2700NKET

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

<b>Location</b>	<b>E-Mail</b>
USA	<a href="mailto:PCNAmericasContact@list.ti.com">PCNAmericasContact@list.ti.com</a>
Europe	<a href="mailto:PCNEuropeContact@list.ti.com">PCNEuropeContact@list.ti.com</a>
Asia Pacific	<a href="mailto:PCNAsiaContact@list.ti.com">PCNAsiaContact@list.ti.com</a>
Japan	<a href="mailto:PCNJapanContact@list.ti.com">PCNJapanContact@list.ti.com</a>