**Vishay Semiconductors** 

# High Voltage Phase Control Thyristor, 12 A



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3L TO-220AB

PRIMARY CHARACTERISTICS					
I <sub>T(AV)</sub> 8 A					
V <sub>DRM</sub> /V <sub>RRM</sub>	800 V				
V <sub>TM</sub>	1.2 V				
I <sub>GT</sub>	15 mA				
TJ	-40 °C to 125 °C				
Package	3L TO-220AB				
Circuit configuration	Single SCR				

### **FEATURES**

- Designed and qualified according to JEDEC<sup>®</sup>-JESD 47
- 125 °C max. operating junction temperature
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **APPLICATIONS**

• Typical usage is in input rectification crowbar (soft start) and AC switch in motor control, UPS, welding, and battery charge.

### DESCRIPTION

The VS-12TTS08... high voltage series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology used has reliable operation up to 125 °C junction temperature.

OUTPUT CURRENT IN TYPICAL APPLICATIONS							
APPLICATIONS	LICATIONS SINGLE-PHASE BRIDGE THREE-PHASE BRIDGE UNITS						
Capacitive input filter $T_A = 55$ °C, $T_J = 125$ °C, common heatsink of 1 °C/W	13.5	17	A				

MAJOR RATINGS AND CHARACTERISTICS					
PARAMETER	TEST CONDITIONS	VALUES	UNITS		
I <sub>T(AV)</sub>	Sinusoidal waveform	8	А		
I <sub>T(RMS)</sub>		12.5	A		
V <sub>DRM</sub> /V <sub>RRM</sub>		800	V		
I <sub>TSM</sub>		110	А		
V <sub>T</sub>	8 A, T <sub>J</sub> = 25 °C	1.2	V		
dV/dt		150	V/µs		
dl/dt		100	A/µs		
TJ	Range	-40 to +125	°C		

VOLTAGE RATINGS							
PART NUMBER	V <sub>RRM</sub> , MAXIMUM PEAK VOLTAGE V	V <sub>DRM</sub> , MAXIMUM PEAK DIRECT VOLTAGE V	I <sub>RRM</sub> ∕I <sub>DRM</sub> AT 125 °C mA				
VS-12TTS08-M3	800	800	1.0				

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## VS-12TTS08-M3

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<b>ABSOLUTE MAXIMUM RATING</b>	S				
PARAMETER	SYMBOL		TEST CONDITIONS	VALUES	UNITS
Maximum average on-state current	I <sub>T(AV)</sub>	T = 109 °C	180° conduction, half sine wave	8	А
Maximum RMS on-state current	I <sub>T(RMS)</sub>	1 <sub>C</sub> = 106 C,	Too conduction, han sine wave	12.5	
Maximum peak, one-cycle,	<b>I</b> =0.1	10 ms sine p	ulse, rated $V_{RRM}$ applied, $T_J$ = 125 °C	95	A
non-repetitive surge current	I <sub>TSM</sub>	10 ms sine p	ulse, no voltage reapplied, $T_J = 125 \ ^{\circ}C$	110	
Maximum I <sup>2</sup> t for fusing	l <sup>2</sup> t	10 ms sine p	ulse, rated $V_{RRM}$ applied, $T_J$ = 125 °C	45	A2-
Maximum Ft for fusing	1-1	10 ms sine p	10 ms sine pulse, no voltage reapplied, $T_J$ = 125 °C		A <sup>2</sup> s
Maximum I <sup>2</sup> $\sqrt{t}$ for fusing	l²√t	t = 0.1 ms to 7	10 ms, no voltage reapplied, $T_J = 125 \ ^\circ C$	640	A²√s
Maximum on-state voltage drop	V <sub>TM</sub>	8 A, T <sub>J</sub> = 25 °C		1.2	V
On-state slope resistance	r <sub>t</sub>	T <sub>.1</sub> = 125 °C		16.2	mΩ
Threshold voltage	V <sub>T(TO)</sub>	1j=125 0		0.87	V
Maximum reverse and direct leakage	1/1	T <sub>J</sub> = 25 °C		0.05	
current	I <sub>RM</sub> /I <sub>DM</sub>	T <sub>J</sub> = 125 °C	$V_{R} = Rated V_{RRM}/V_{DRM}$	1.0	
Typical holding current	Ι <sub>Η</sub>	Anode supply = 6 V, resistive load, initial $I_T$ = 1 A, $T_J$ = 25 °C		30	mA
Maximum latching current	١L	Anode supply = 6 V, resistive load, $T_J = 25 \text{ °C}$		50	
Maximum rate of rise of off-state voltage	dV/dt	$T_J = T_J max.$ , linear to 80 °C, $V_{DRM} = R_g - k = Open$		150	V/µs
Maximum rate of rise of turned-on current	dl/dt			100	A/µs

TRIGGERING					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum peak gate power	P <sub>GM</sub>		8.0	w	
Maximum average gate power	P <sub>G(AV)</sub>		2.0	vv	
Maximum peak positive gate current	+ I <sub>GM</sub>		1.5	А	
Maximum peak negative gate voltage	- V <sub>GM</sub>		10	V	
	I <sub>GT</sub>	Anode supply = 6 V, resistive load, $T_J$ = - 65 °C	20		
Maximum required DC gate current to trigger		Anode supply = 6 V, resistive load, $T_J = 25 \ ^{\circ}C$	15	mA	
		Anode supply = 6 V, resistive load, $T_J$ = 125 °C	10		
		Anode supply = 6 V, resistive load, $T_J$ = - 65 °C	1.2		
Maximum required DC gate voltage to trigger	V <sub>GT</sub>	Anode supply = 6 V, resistive load, $T_J = 25 \text{ °C}$	1	v	
		Anode supply = 6 V, resistive load, $T_J$ = 125 °C	0.7	v	
Maximum DC gate voltage not to trigger	V <sub>GD</sub>				
Maximum DC gate current not to trigger	I <sub>GD</sub>	T <sub>J</sub> = 125 °C, $V_{DRM}$ = Rated value 0.1		mA	

SWITCHING						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Typical turn-on time	t <sub>gt</sub>	T <sub>J</sub> = 25 °C	0.8			
Typical reverse recovery time	t <sub>rr</sub>	T. = 125 °C	3	μs		
Typical turn-off time	tq	1J = 125 C	100			

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THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		-40 to 125	°C	
Maximum thermal resistance, junction to case		R <sub>thJC</sub>	DC operation	1.5		
Maximum thermal resistance, junction to ambient		R <sub>thJA</sub>		62	°C/W	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.5		
Approximate weight				2	g	
Approximate weight				0.07	oz.	
Mounting torque	minimum			6 (5)	kgf ⋅ cm	
Mounting torque	maximum			12 (10)	(lbf ⋅ in)	
Marking device Case style 3L TO-220AB 121		12TT	rS08			

Maximum Average On-State Power Loss (W)

Maximum Average On-State Power Loss (W)

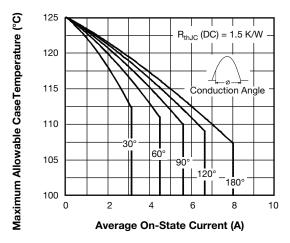


Fig. 1 - Current Ratings Characteristics

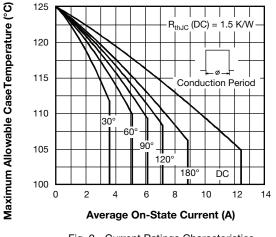


Fig. 2 - Current Ratings Characteristics

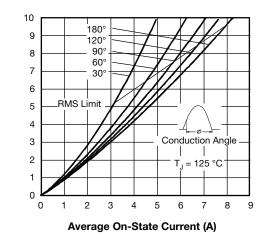


Fig. 3 - On-State Power Loss Characteristics

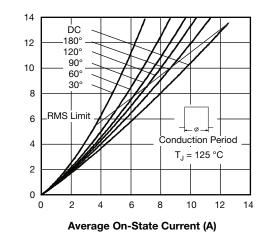


Fig. 4 - On-State Power Loss Characteristics

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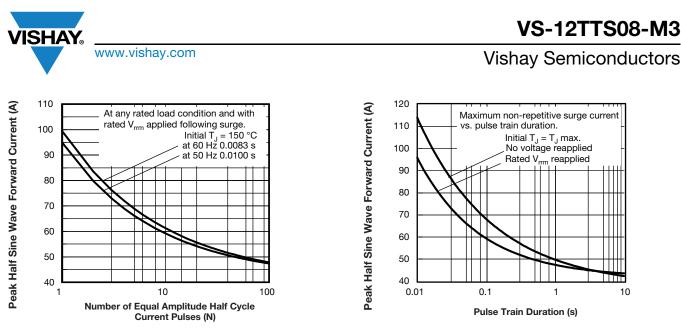
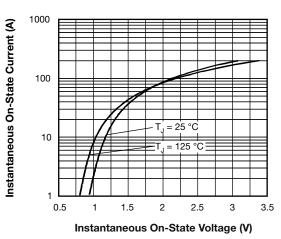


Fig. 5 - Maximum Non-Repetitive Surge Current







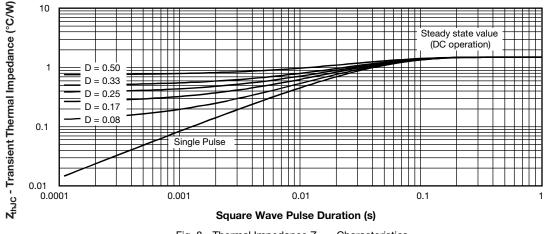
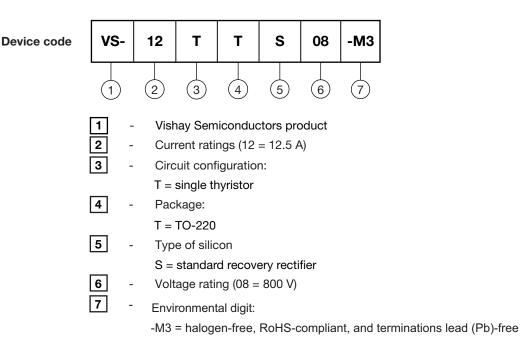


Fig. 8 - Thermal Impedance Z<sub>thJC</sub> Characteristics



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### **ORDERING INFORMATION TABLE**



ORDERING INFORMATION (Example)						
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-12TTS08-M3	50	1000	Antistatic plastic tubes			

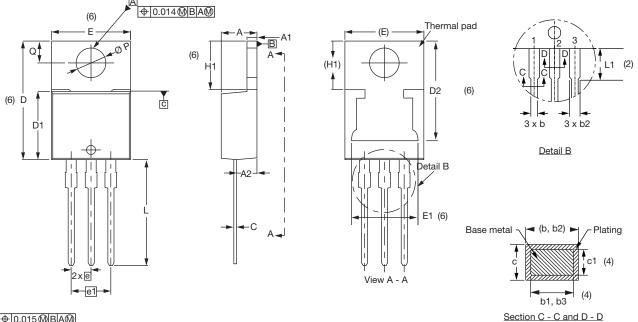
LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?96154				
Part marking information	www.vishay.com/doc?95028			



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## **3L TO-220AB**

#### **DIMENSIONS** in millimeters and inches



⊕0.015@BA@





SYMBOL	MILLIMETERS INCHI		HES	NOTES		
STINDUL	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.25	4.65	0.167	0.183		
A1	1.14	1.40	0.045	0.055		
A2	2.50	2.92	0.098	0.115		
b	0.69	1.01	0.027	0.040		
b1	0.38	0.97	0.015	0.038	4	
b2	1.20	1.73	0.047	0.068		
b3	1.14	1.73	0.045	0.068	4	
С	0.36	0.61	0.014	0.024		
c1	0.36	0.56	0.014	0.022	4	
D	14.85	15.35	0.585	0.604	3	
D1	8.38	9.02	0.330	0.355		

_		
Conforms to JEDEC <sup>®</sup>	outline	<b>TO-220AB</b>

SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES
D2	11.68	13.30	0.460	0.524	6, 7
Ш	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØР	3.54	3.91	0.139	0.154	
Q	2.60	3.00	0.102	0.118	

#### Notes

<sup>(2)</sup> Lead dimension and finish uncontrolled in L1

- <sup>(4)</sup> Dimension b1, b3, and c1 apply to base metal only
- (5) Controlling dimensions: inches
- <sup>(6)</sup> Thermal pad contour optional within dimensions E, H1, D2, and E1
- <sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> TO-220, except D2

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 $<sup>^{(1)}\,</sup>$  Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(3)</sup> Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body



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