ASAKDVAIG

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2.0 x 1.6 x 0.8 mm **RoHS/RoHS II Compliant** MSL Level = 1

Features

- Continuous V_{DD} operation from 1.62 V ~ 3.63 V
- Optimized for low current consumption
- Output Enable/Start & Disable/Stop function
- Output waveform CMOS/LVCMOS compatible
- Hermetically seam-sealed ceramic package
- AEC-Q200 Qualified
- Automotive Grade 1: -40°C to +125°C
- TS16949 Production Line Certified
- PPAP Available Upon Request

Applications

- Infotainment Systems
- Keyless Entry & Startup
- GPS & Navigation
- Comfort control
- ADAS (Advanced Driver Assistance Systems)
- Vehicle to Vehicle Communication
- LiDAR (Light Detection and Ranging)
- In-vehicle Networking
- Powertrain & Drive Control
- Power Control & Conversion
- Industrial Control & Automation

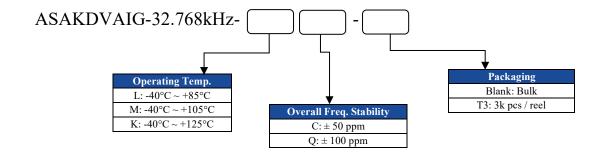
Electrical Specifications

Parameters		Min.	Typ.	Max.	Units	Notes
Frequency		32.768			kHz	
Operating Temperature Range		-40.0		+125	°C	See options
Storage Temperature Range		-55.0		+125	°C	
Overall Frequency Stability [Note 1]		-100		+100	ppm	See options
Supply Voltage (Vdd)		+1.62		+3.63	V	
Tri-state function [Note 2]		"1" (VIH≥0.7*Vdd) or Open: Oscillation; "0" (VIL<0.3*Vdd): No Oscillation/Hi Z			V	
Output Load				15	рF	CMOS
Output Voltage	V_{OH}	0.9*Vdd			V	
Output Voltage	$ m V_{OL}$			0.1*Vdd		
Aging 1 year @25°C±3°C		-3.0		+3.0	ppm	
Aging 5 years @25°C± 3°C		-5.0		+5.0	ppm	
Symmetry @ ½ Vdd		45	50	55	%	
Start-up Time				10	ms	
Rise and Fall Time (Tr/Tf) @10%Vdd-90%Vdd, 15pF load				50	ns	
Disable Current				10	μΑ	
Supply Current (Idd) @25°C± 3°C	Vdd = 3.3V		80	100	μΑ	No Load
	Vdd = 2.5V		70	90	μΑ	No Load
	Vdd = 1.8V		60	80	μΑ	No Load

Overall frequency stability includes initial frequency tolerance @25°C± 3°C and stability over the operating temperature range.

Do not leave pin 1 $(\overline{\text{INH}})$ floating. If pin 1 $(\overline{\text{INH}})$ is not utilized for toggling, it must be tied to Vdd (logic 1). Note 2:

Part Identification





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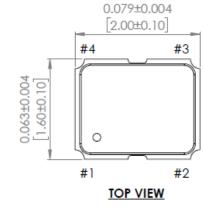
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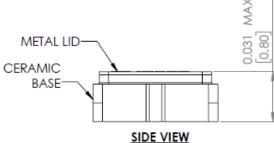
ESD Sensitive

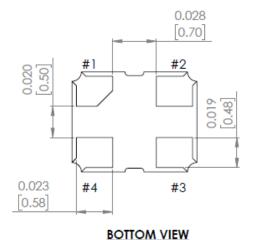


2.0 x 1.6 x 0.8 mm RoHS/RoHS II Compliant MSL Level = 1

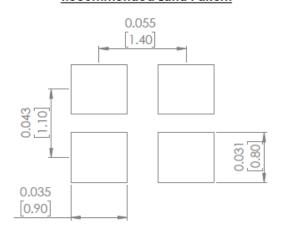
Mechanical Dimensions







Recommended Land Pattern



Pin#	Function		
1	ĪNH		
2	GND		
3	Output		
4	Vdd		

INH Function					
#1	#3 (Output)				
Open	Active				
"H" Level	Active				
"L" Level	High Z (No Oscillation)				

Note 3

-Do not leave Pin 1 (INH) floating

-If Pin 1 ($\overline{\text{INH}}$) is not utilized for toggling, it must be tied to Vdd (logic 1)

Note 4:

Recommended to use approximately $0.01 \mu F$ bypass capacitor between PIN 2 and PIN 4

Dimensions: inches (mm)



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ESD Sensitive (Pb)



2.0 x 1.6 x 0.8 mm RoHS/RoHS II Compliant MSL Level = 1

Reflow Profile [JDEC J-STD-020]

Temperature

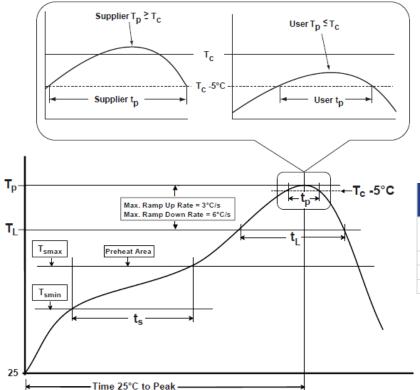


Table 1 SnPb Eutectic Process Classification Temperatures (Tc) Package Thickness Volume mm³ 3 Volume mm³ 2350 <2.5 mm</td> 235 °C 220 °C ≥2.5 mm 220 °C 220 °C

Table 2 Pb-Free Process

>2.5 mm

Classification Temperatures (1c)					
Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000		
<1.6 mm	260 °C	260 °C	260 °C		
1.6 mm - 2.5 mm	260 °C	250 °C	245 °C		

245 °C

250 °C

Sn-Pb Eutectic Assembly	Pb-Free Assembly
100°C	150°C
150°C	200°C
60 - 120 sec.	60 - 120 sec.
3°C/sec. max	3°C/sec. max
183°C	217°C
60 - 150 sec.	60 - 150 sec.
see Table 1	see Table 2
20 sec.	30 sec.
6°C/sec. max	6°C/sec. max
6 min. max	8 min. max
	100°C 150°C 60 - 120 sec. 3°C/sec. max 183°C 60 - 150 sec. see Table 1 20 sec. 6°C/sec. max

^{*}Tolerance for peak profile temperature (T_P) is defined as a supplier minimum and a user maximum.



245 °C

^{**}Tolerance for time at peak profile temperature (t_p) is defined as supplier minimum and a user maximum.

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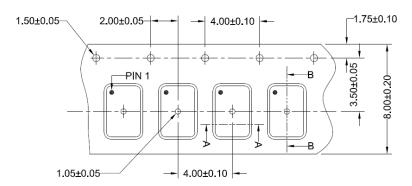
ESD Sensitive

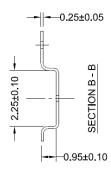


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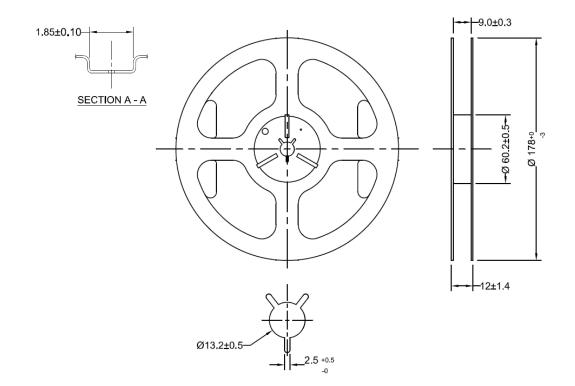
Packaging

T3: 3,000pcs/reel





FEEDING (PULL) DIRECTION



Dimensions: mm

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