



REVISION HISTORY				
ECO	REV	DESCRIPTION	DATE	APPROVED

MEC8-188-02-L-D-EDGE_CONNRE-DIM
J1E J10

VERSION TABLE			
ASSEMBLY TYPE	U2	BITS	CLOCK
DC896A-D	LTC2208LVDS	16	138MHz
DC896A-B	LTC2208LVDS-14	14	138MHz
DC896A-R	LTC2208LVDS	16	138MHz
DC896A-S	LTC2208LVDS-14	14	138MHz

CUSTOMER NOTICE
 LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS. HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SUBSTANTIALLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.
 THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

CONTRACT NO.
APPROVALS
 DRAWN
 CHECKED
 APPROVED
 ENGINEER
 DESIGNER
 FILED
 DATE: 8/24/2006 04:01:49p

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 LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC
LTC22208 FAMILY LVDS BUS
SIZE: B **DIWG NO.: DENO CIRCUIT 986A** **REV: 2**
DATE: 8/24/2006 04:01:49p **SHEET 1 OF 1**