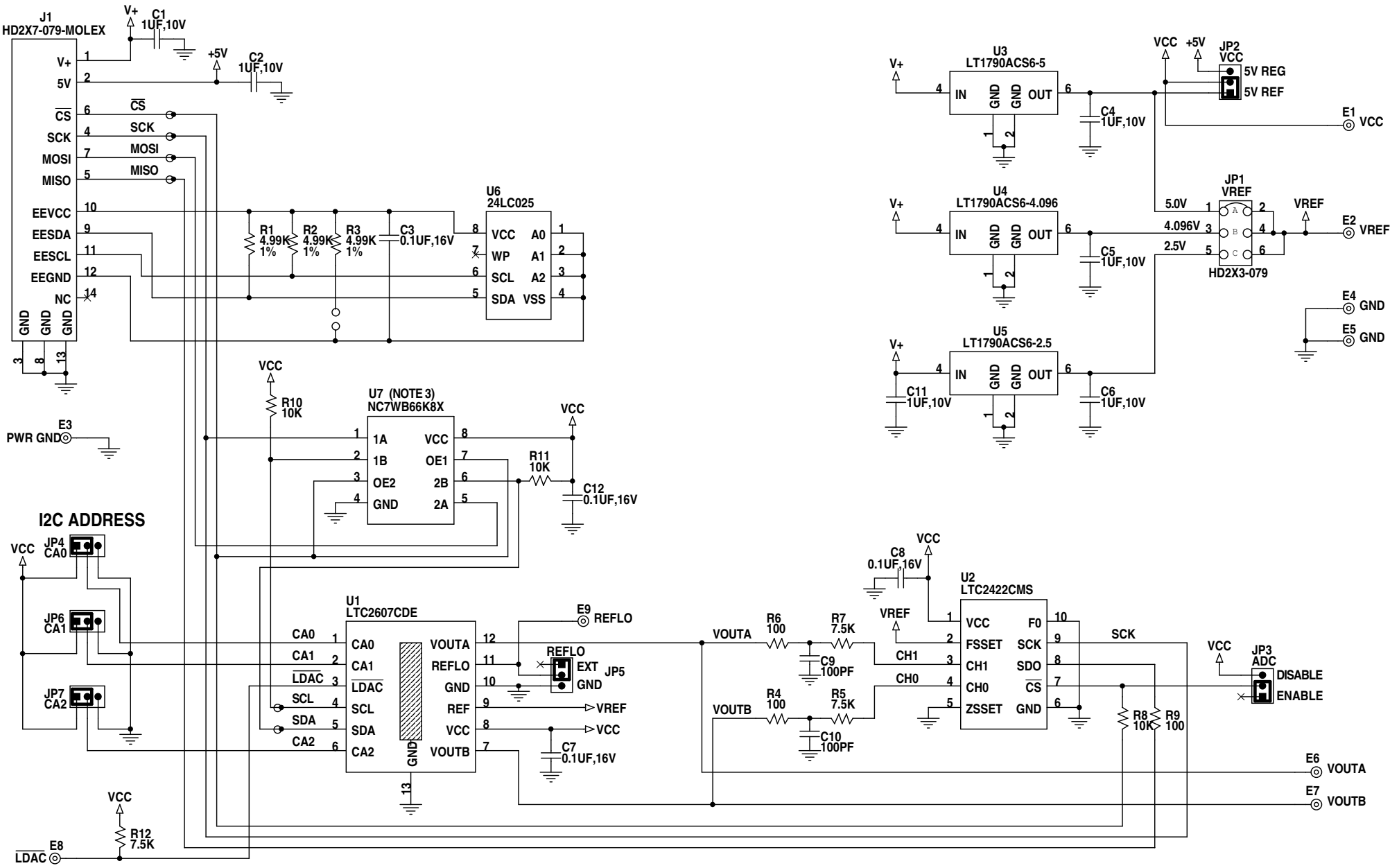

NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 0402.
   ALL CAPACITORS ARE IN MICROFARADS, 0402.
2. INSTALL SHUNTS ON JP1-JP7 PIN 1 AND 2.
3. U7 MULTIPLEXES THE SPI AND I2C BUSSES AND IS FOR COMPATIBILITY WITH THE DC590 CONTROLLER BOARD ONLY.

CONTRACT NO.

APPROVALS

DRAWN: KIM T.

CHECKED:

APPROVED:

ENGINEER: MARK T.

DESIGNER:

LINEAR TECHNOLOGY

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TITLE: SCHEMATIC

16-BIT DUAL RAIL-TO-RAIL DAC WITH I2C INTERFACE

SIZE	DWG NO.	REV
A	DC934A-LTC2607CDE	A

DATE: Monday, September 26, 2005

SHEET 1 OF 1