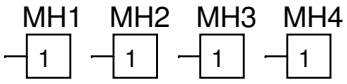
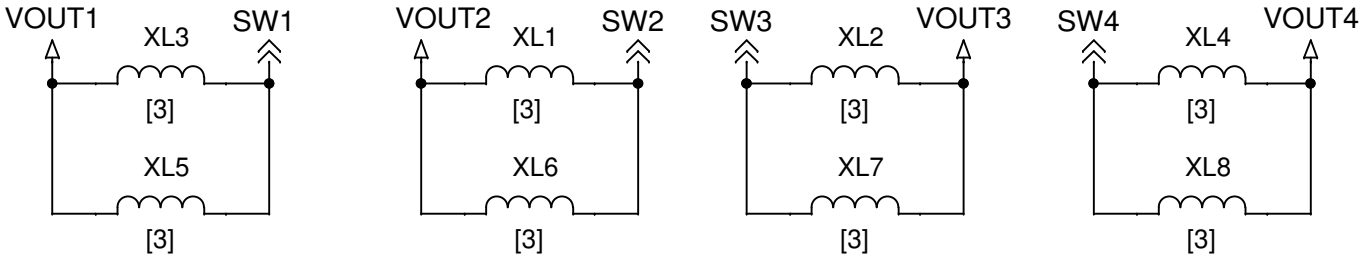
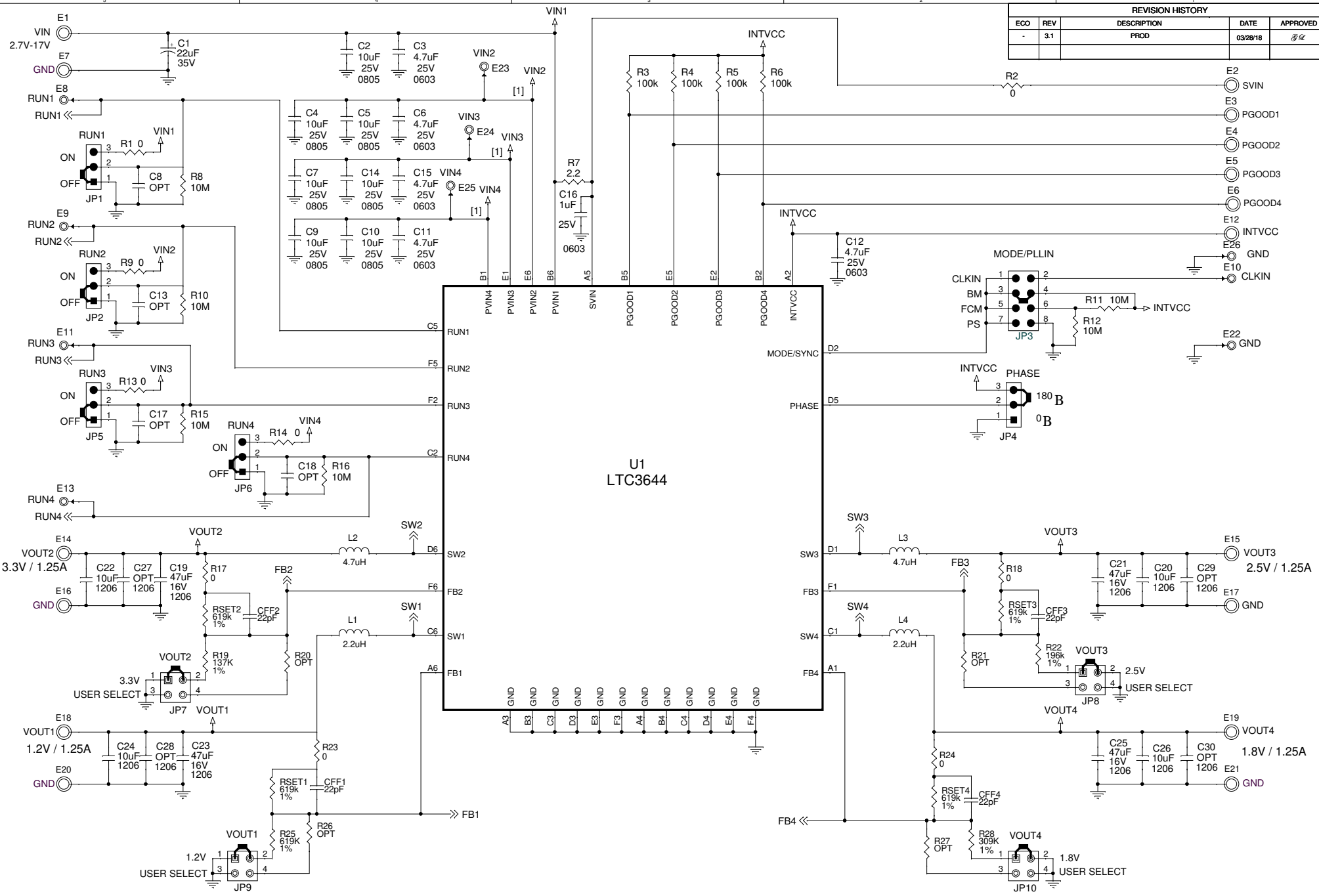
USED TO MANUFACTURE PCB



REPRESENTS STAND OFFS





REVISION HISTORY				
ECO	REV	DESCRIPTION	DATE	APPROVED
-	3.1	PROD	03/28/18	

NOTES: UNLESS OTHERWISE SPECIFIED:
1. All Resistors and Capacitors are 0402.

VERSION	IC	INDUCTORS	FSW
-A	LTC3644	L1, L4 COILCRAFT XFL4020-222ME L2, L3 COILCRAFT XFL4020-472ME	1MHz

APPROVALS	
PCB DES.	
APP ENG.	

ANALOG DEVICES www.analog.com
POWER BY LINEAR www.linear.com
1630 McCarthy Blvd.
Milpitas, CA 95035
Phone: 1-408-432-1900

IC NO. LTC3644

TITLE: DEMO CIRCUIT SCHEMATIC,
QUAD 17V, 1.25A SYNCHRONOUS STEP-DOWN REGULATOR

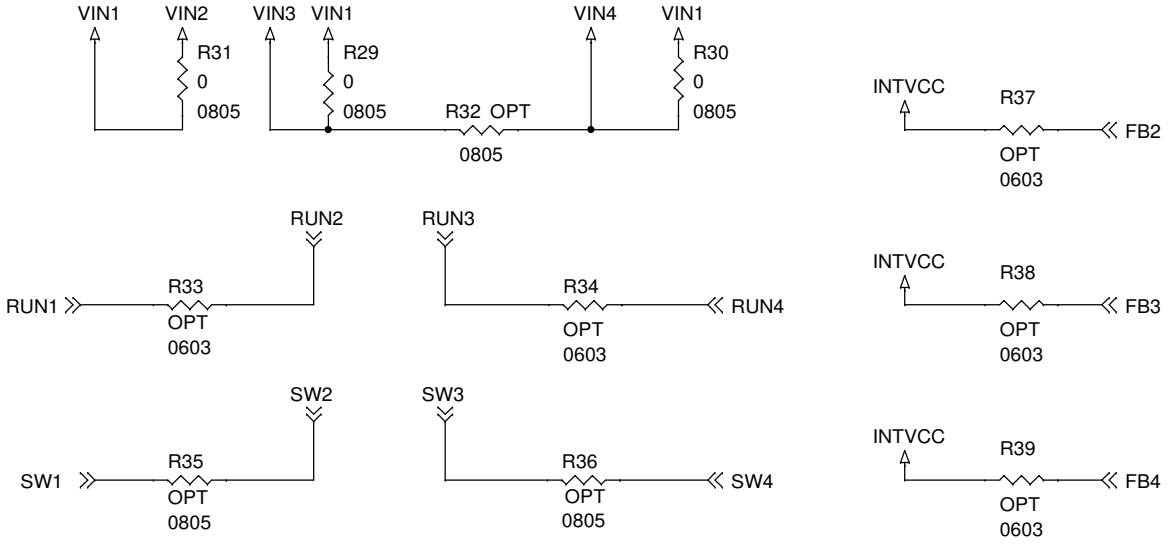
SKU NO. DC2383A-A

PCA BOM: 700-DC2383A-A_REV03
PCA ASS'Y: 705-DC2383A-A_REV03

SCHEMATIC NO. AND REVISION: 710-DC2383A-A_REV03.1

SIZE: N/A | SCALE = NONE | DATE: Monday, May 18, 2020 | SHEET 1 OF 2

OPTIONAL JUMPERS FOR PARALLELING PHASES FOR DESIRED NUMBER OF OUTPUT VOLTAGE RAILS (SHARING THE SAME VIN1)



NUMBER OF OUTPUT VOLTAGE RAILS		Paralleling Channel	Master Channel	0 OHM (Required)	OPT (Do not stuff)	PHASE (JP13)
QUAD 1.25A	4	1/2/3/4			R33, R34, R35, R36, R37, R38, R39	INTVCC
TRIPLE 2.5A/1.25A/1.25A	3	1+2/3/4	1	R33, R35, R37	R4, R9, R10, R17, RSET2, CFF2, R19, R34, R36, R38, R39, L2, JP2	INTVCC
DUAL 2.5A/2.5A	2	1+2/3+4	1, 4	R33, R34, R35, R36, R37, R38	R4, R5, R9, R10, R13, R15, R17, R18, RSET2, RSET3, CFF2, CFF3, R19, R22, R39 L2, L3, JP2, JP5	INTVCC

NOTE:
Please refer to the data sheet and demo board manual for more details and examples of paralleling phases to obtain the desired number of output voltage rails.

APPROVALS	
PCB DES.	MF
APP ENG.	BL

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SIZE: N/A | SCALE = NONE | DATE: Monday, May 18, 2020 | SHEET 2 OF 2