



## Customer Information Notification

202103007I : PF7100 Power Management IC Data Sheet Updates for Output Accuracy Spec Improvement and Clarifications

**Note:** This notice is NXP Company Proprietary.

**Issue Date:** Mar 17, 2021 **Effective date:** Mar 18, 2021

Dear Collette Sannes-Neste,

Here is your personalized notification about a NXP general announcement.  
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### Change Category

<input type="checkbox"/> Wafer Fab Process	<input type="checkbox"/> Assembly Process	<input type="checkbox"/> Product Marking	<input type="checkbox"/> Test Process	<input type="checkbox"/> Design
<input type="checkbox"/> Wafer Fab Materials	<input type="checkbox"/> Assembly Materials	<input type="checkbox"/> Mechanical Specification	<input type="checkbox"/> Test Equipment	<input type="checkbox"/> Errata
<input type="checkbox"/> Wafer Fab Location	<input type="checkbox"/> Assembly Location	<input type="checkbox"/> Packing/Shipping/Labeling	<input type="checkbox"/> Test Location	<input type="checkbox"/> Electrical spec./Test coverage
<input type="checkbox"/> Firmware	<input checked="" type="checkbox"/> Other: Data Sheet Update (Specification Improvement and Clarifications)			

## PCN Overview

### Description

NXP Semiconductors announces the data sheet update to revision 4.0 for the PF7100 family devices, including all applicable derivative products, associated with this notification.

The new data sheet revision provides updated specification information and clarifications, including the tightening of the output accuracy specification on all Type 1 switching regulators, as well as increasing the storage temperature range.

The revision history included in the updated document provides a detailed description of the changes. Changes are summarized below.

PF7100 Data Sheet Rev 4.0 Change Summary:

1. Table 50: Output accuracy of the Type 1 Buck regulators (SW1 - SW4) is split in four operating ranges to allow tighter accuracy at higher output voltage configuration. Output voltages from 0.8V to 1.0V provide a specification improvement from +/-2.0% to +/-1.5% output accuracy.
2. Table 6: The minimum storage temperature is extended from -40C to -55C.
3. Figure 22: Corrected to depict the proper behavior of the XFAILB when the PWRON pin is still asserted low.

\*\* Documentation changes only for tighter (better) switching regulator output accuracy, storage

temperature, and technical clarifications, corrections - absolutely no changes to the device / product  
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PF7100 new revision 4.0 data sheet is attached to this notice and is available at:  
<https://www.nxp.com/docs/en/data-sheet/PF7100.pdf>

Corresponding ZVEI Delta Qualification Matrix ID: SEM-DS-02

#### **Reason**

Switching regulator accuracy specification is tightened to provide specification improvement on output voltages from 0.8 to 1.0V. Storage temperature range is increased to provide improvement on the minimum temperature. XFAILB figure is corrected.

#### **Identification of Affected Products**

Product identification does not change

#### **Anticipated Impact on Form, Fit, Function, Reliability or Quality**

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No Impact on form, fit, function, reliability or quality

#### **Data Sheet Revision**

A new datasheet will be issued

#### **Contact and Support**

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For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

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**Affected OPNS**

MPF7100AVMA0ESR2	MPF7100BVBA0ES	MPF7100BVMA2ESR2	MPF7100BVBA3ES
MPF7100AVMA0ES	MPF7100BMMA0ESR2	MPF7100BVMA2ES	MPF7100BVMA4ESR2
MPF7100AVBA0ESR2	MPF7100BMMA0ES	MPF7100BVBA1ESR2	MPF7100BVMA4ES
MPF7100AVBA0ES	MPF7100BMBA0ESR2	MPF7100BVBA1ES	MPF7100BVMA3ESR2
MPF7100BVMA0ESR2	MPF7100BMBA0ES	MPF7100BVBA2ESR2	MPF7100BVMA3ES
MPF7100BVMA0ES	MPF7100BVMA1ESR2	MPF7100BVBA2ES	MPF7100BVBA4ESR2
MPF7100BVBA0ESR2	MPF7100BVMA1ES	MPF7100BVBA3ESR2	MPF7100BVBA4ES