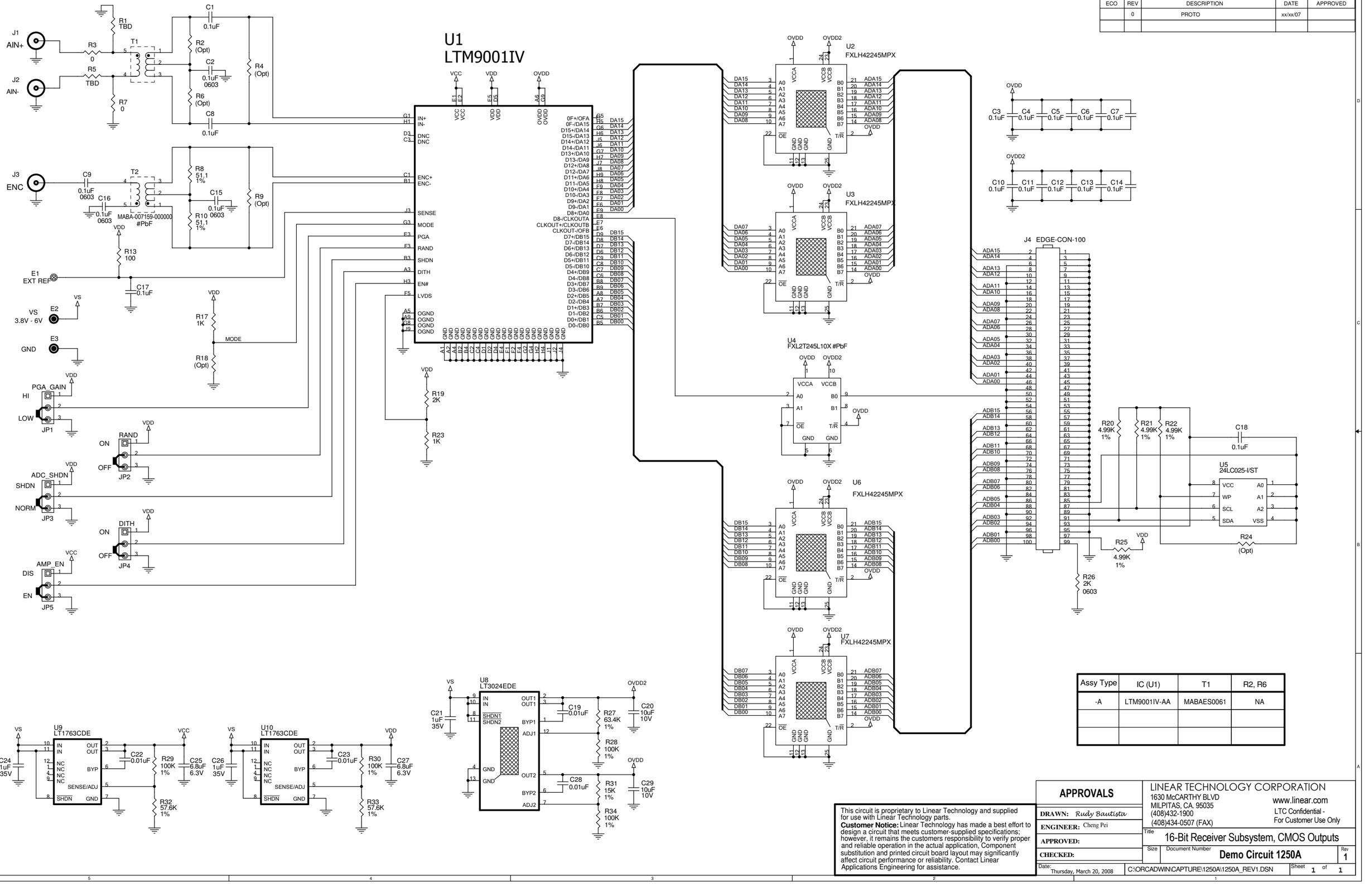
REVISION HISTORY				
ECO	REV	DESCRIPTION	DATE	APPROVED
	0	PROTO	xx/xx/07	



Assy Type	IC (U1)	T1	R2, R6
-A	LTM9001IV-AA	MABAES0061	NA

This circuit is proprietary to Linear Technology and supplied for use with Linear Technology parts.
Customer Notice: Linear Technology has made a best effort to design a circuit that meets customer-supplied specifications; however, it remains the customers responsibility to verify proper and reliable operation in the actual application. Component substitution and printed circuit board layout may significantly affect circuit performance or reliability. Contact Linear Applications Engineering for assistance.

APPROVALS

DRAWN: Rudy Bautista
ENGINEER: Cheng Pei
APPROVED:
CHECKED:
Date: Thursday, March 20, 2008

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Title: 16-Bit Receiver Subsystem, CMOS Outputs
Size | Document Number: Demo Circuit 1250A | Rev: 1
C:\ORCADWIN\CAPTURE\1250A\1250A_REV1.DSN
Sheet 1 of 1