

ClearFog Base

Product Change Notification

PCN# 20200212001



Date of publication: December 12th, 2020

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Background

Dear customer,

SolidRun Ltd. is discontinuing production of its ClearFog Base Rev-1.2 products and following the introduction of the ClearFog Base Rev-1.3, recommends migrating to that platform, keeping the same form factor.

You are receiving this notification, because you have purchased one or more of SolidRun Ltd. products affected by the discontinuation, during the past twenty-four (24) months.

Time Frame

- Switch to new revision - effective immediately.
- Last time buy – 12th August 2020.

Affected SKUs

The affected SKUs are all the SKUs for ClearFog Base Rev-1.2. Please see detailed information below on the affected SKUs based on the following.

SKU	New SKU	New Description
SRM6828S00D01GE008B01CH	SRM6828S00D01GE008B02CH	SOM A388 Dual ClearFog Base No WiFi/BT 1GB RAM 8GB eMMC Com.Temp R01 Inc. Heatsink
SRM6828S00D01GE016B01CH	SRM6828S00D01GE016B02CH	SOM A388 Dual ClearFog Base No WiFi/BT 1GB RAM 16GB eMMC Com.Temp R02 Inc. Heatsink
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Description of Changes

Functional:

Based on customer feedback and requests, we decided to improve support of dual cellular SIM card support to allow for one of the following options:

- SIM cards connected to up to two modems in a direct electrical connection.
- Two SIM cards to one modem through SIM card mux.

In addition, some other changes were made to fix bugs related to M.2 support of cellular modems.

Software visible changes:

U15 EEPROM is populated by default. The EEPROM is on I2C0 bus at address 0x52. This EEPROM stores board identification information written at manufacturing.

This revision adds an optional SIM card selector multiplexer that allows run-time switch between two SIM cards. The multiplexer can be connected at manufacturing (controlled by resistors) to either the mini-PCIe slot (CON3) or the M.2 slot (CON10). Software can toggle the logic level of Armada 388 MPP41 pin to select the active SIM card. The multiplexer is not assembled by default.

M.2 slot (CON10) pin #6 is now driven by I2C IO expander pin 0_2. The signal is pulled low on power up.

M.2 slot (CON10) pin #67 is now pulled low on power up. High logic voltage level of pin #67 is now 1.8V.

Transition:

SolidRun recommends its customers to transition to the ClearFog Base Rev-1.3. The online store is updated with the new part numbers and customers can order samples.

Contact:

Please contact SolidRun if you have any questions.

For commercial and sales inquiries please email to: sales@solid-run.com

For technical questions please email to: support@solid-run.com