

LDBCA2100JC5N0

Aliases (DBCA2100JC5N0)

LDB, Film, Metallized PPS Stacked, General Purpose, 0.01 uF, 5%, 50 VDC, 125°C, 1206, 1.1mm



Click [here](#) for the 3D model.

Dimensions

Chip Size	1206
L	3.3mm +0.3/-0.1mm
W	1.7mm +/-0.2mm
T	1.1mm MAX
B	0.4mm +0.5/-0.3mm

Packaging Specifications

Packaging	T&R
Packaging Quantity	3000

General Information

Series	LDB
Dielectric	Metallized PPS Stacked
Style	SMD Chip
RoHS	Yes
AEC-Q200	No
Component Weight	0.06 g
Shelf Life	104 Weeks
MSL	3

Specifications

Capacitance	0.01 uF
Capacitance Tolerance	5%
Voltage DC	50 VDC
Temperature Range	-55/+125°C
Rated Temperature	125°C
Dissipation Factor	0.6% 1kHz
Insulation Resistance	3 GOhms