



## CUSTOMER ADVISORY

### JTAG Interface Timing Change for Titanium™ Ti60 and Ti35 FPGA

<b>Advisory Details</b>	<p>Below timings in the JTAG mode have been changed for optimum performance:</p> <ul style="list-style-type: none"> <li>• Reduced TCK frequency (<math>f_{TCK}</math>) to 10MHz maximum.</li> <li>• Increased TDI setup time (<math>t_{TDISU}</math>) and TMS setup time (<math>t_{TMSSU}</math>) to 15ns minimum.</li> <li>• Increased TCK falling edge to TDO output (<math>t_{TCKTDO}</math>) to 30ns maximum.</li> </ul> <p>Customers can refer to the following collaterals for details:</p> <ul style="list-style-type: none"> <li>• <a href="#">Ti35 Data Sheet</a></li> <li>• <a href="#">Ti60 Data Sheet</a></li> </ul>
<b>Parts Affected</b>	<p>Titanium devices and variants shown below:</p> <ul style="list-style-type: none"> <li>• Ti35/Ti60F225XX</li> <li>• Ti35/Ti60F100S3F2XX</li> <li>• Ti60W64XX</li> </ul> <p>XX = All speed grades</p>
<b>Contact:</b>	<p>For more information, please contact your local Sales or FAE or contact us online at <a href="https://www.efinixinc.com/company-contact-us.html">https://www.efinixinc.com/company-contact-us.html</a>.</p>